

Languages for Programming Hybrid Discrete/Continuous-Time Systems

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In collaboration with Albert Benveniste, **Timothy Bourke**, Benoit
Caillaud and Bruno Pagano.

Current Practice

Hybrid Systems Modelers

Some issues

Interlude: interacting with a numerical solver

Key elements of our approach

Non Standard Synchronous Semantics

Typing

Causality

Compilation

Trends for building **safe and complex** embedded systems

Write **executable mathematical specifications** in a high-level language so that a model is:

A **reference semantics** independent of any implementation.

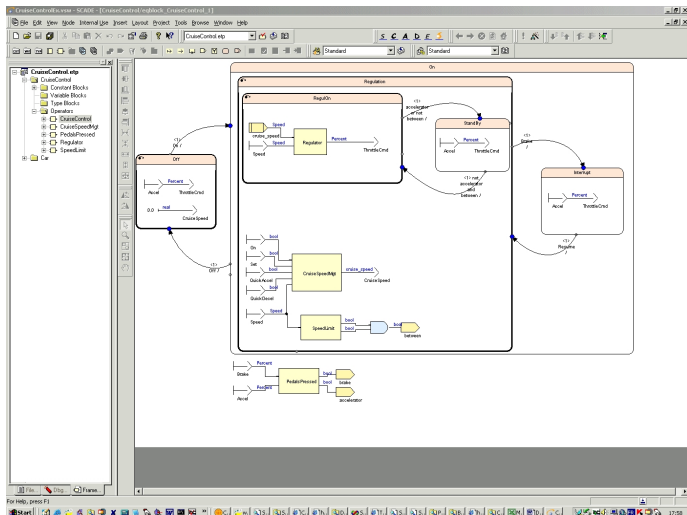
A base for **simulation, testing, formal verification**.

Then **compiled** into executable code, **sequential** or **parallel**.

A way to achieve **correct-by-construction** software.

Synchronous Block Diagram Languages¹

E.g., The Cruise control in SCADE 6 (Esterel-Technologies/ANSYS).



¹Cf. previous courses by Gérard Berry.

A good match for programming **discrete-time** controllers

Their semantics is **simple** and mathematically **precise**:

Difference equations; hierarchical automata; parallel composition.

Simulate/test/verify throughout the development process.

Then compiler ensures strong **safety properties**.

The program is deterministic.

The generated code runs in bounded time and memory.

Efficient and fully **traceable** code generation.

The code is correct w.r.t the source model.

Meets the highest quality level of civil avionics (DO178C, level A).²

SCADE 6 is used for programming various **critical control software**.³

²Cf. Seminar by Bruno Pagano, in Spring 2013.

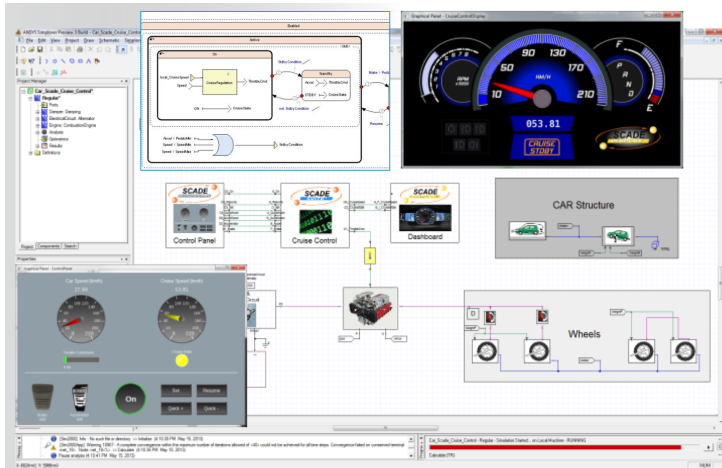
³Cd. Seminar by Emmanuel Ledinot, in Spring 2013.

But modern systems need more...

The Current Practice of Hybrid Systems Modeling

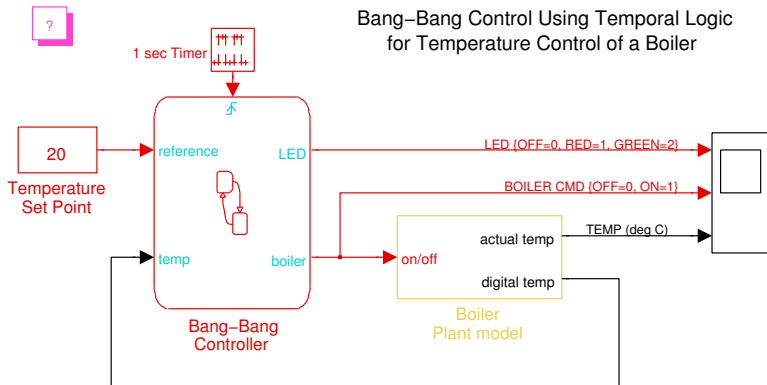
Embedded software interacts with physical devices.

The whole system has to be modeled: the controller and the plant.⁴



⁴Image by Esterel-Technologies/ANSYS.

Example: a Bang-bang controller [demo].



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A Wide Range of Hybrid Systems Modelers Exist

Ordinary Differential Equations + discrete time:

Simulink/Stateflow ($\geq 10^6$ licences), LabView, Ptolemy II, etc.

Differential Algebraic Equations + discrete time:

Modelica, VHDL-AMS, VERILOG-AMS, etc.

Dedicated tools for multi-physics:

Mechanics, electro-magnetics, fluid, etc.

Co-simulation/combination of tools:

Agree on a common format/protocol: FMI/FMU, S-functions, etc.

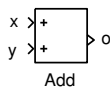
Convert the model of one tool into another.

Underlying Mathematical Models

Synchronous parallelism, sequence equations:⁵

Time is **discrete and logical** (indices in \mathbb{N})

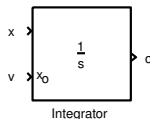
Equation $o = x + y$ means $\forall n \in \mathbb{N}. o(n) = x(n) + y(n)$



Ordinary Differential Equations (ODEs):⁶

Time is **continuous** (indices in \mathbb{R})

Equation $o = \frac{1}{s}(x)$ *init* v means $\forall t \in \mathbb{R}. o(t) = v(0) + \int_0^t x(\tau) d\tau$



⁵Cf. Course by Gerard Berry, Spring 2013.

⁶Cf. Seminar by Juliette Leblond, Feb. 2014.

Is there anything left to do?

We know how to build tools for discrete-time models.

We know how to build tools for continuous-time models.

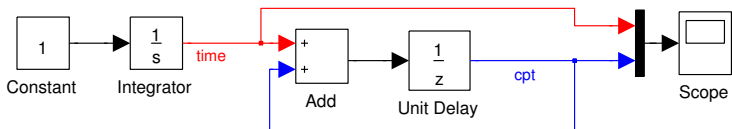
But what if **the two are mixed together?**

Is it enough to **connect one to the other?**

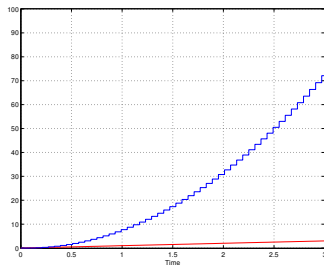
Can you trust **code automatically generated** from such tools?

Strange beasts...

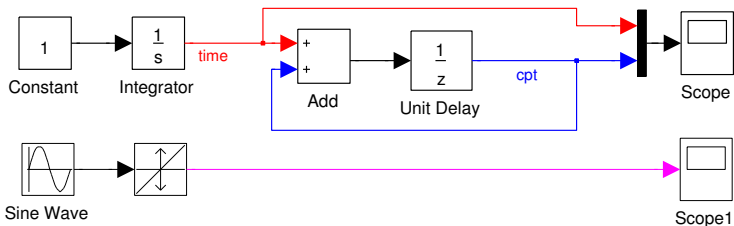
Typing issue 1: Mixing continuous & discrete components



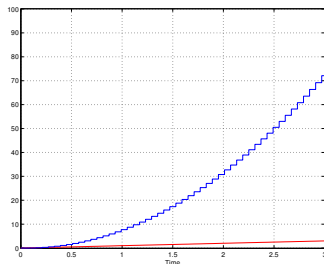
Basic model



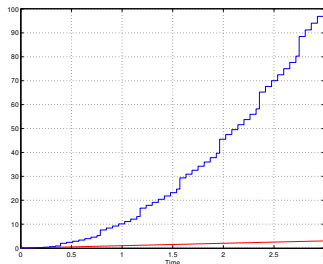
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Basic model

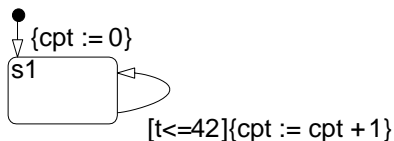
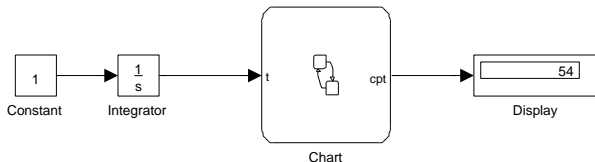


with Sine Wave



- The shape of `cpt` depends on the steps chosen by the solver.
- Putting another component in parallel can change the result.

Typing issue 2: Boolean guards in continuous automata

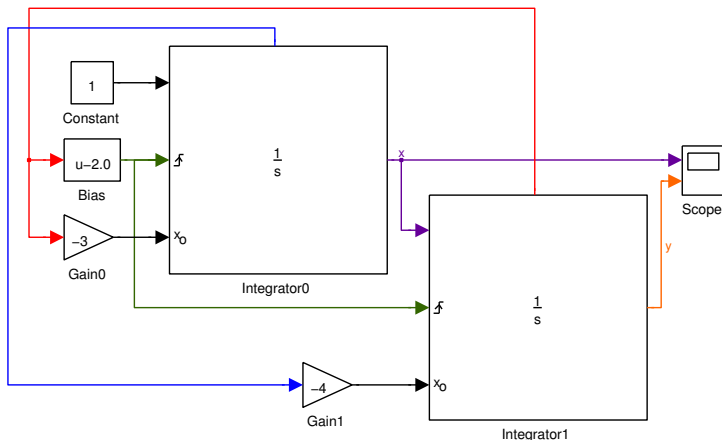


How long is a discrete step?

- Adding a parallel component changes the result.
- No warning by the compiler.
- The manual says: “A single transition is taken per major step”.

Discrete time is not logical: it is that of the simulation engine.

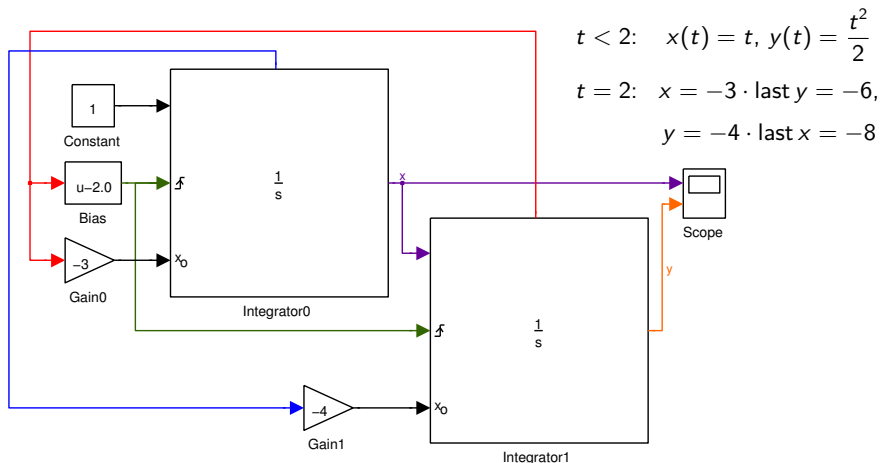
Causality issue: the Simulink state port



The output of the state port is the same as the output of the block's standard output port except for the following case. If the block is reset in the current time step, the output of the state port is the value that would have appeared at the block's standard output if the block had not been reset.

–Simulink Reference (2-685)

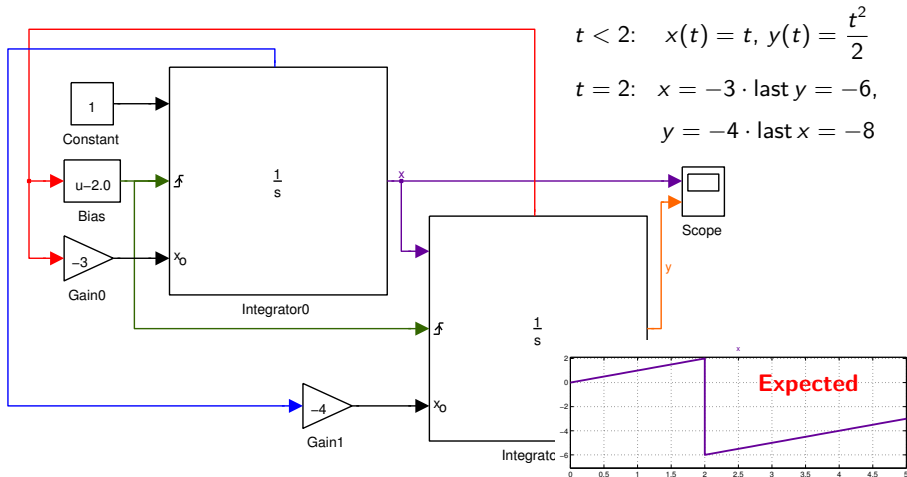
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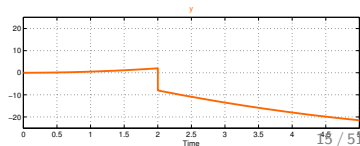
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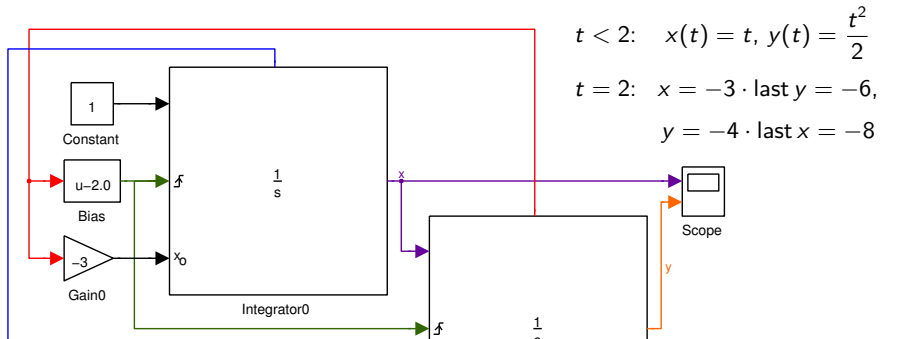


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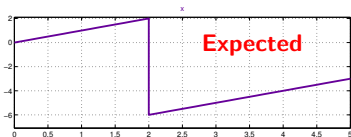
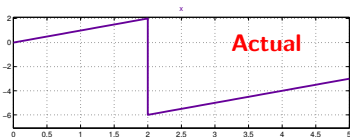


Causality issue: the Simulink state port

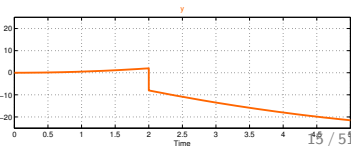
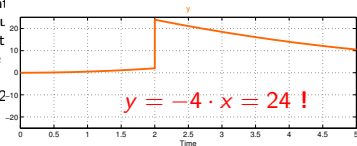


$$t < 2: \quad x(t) = t, \quad y(t) = \frac{t^2}{2}$$

$$t = 2: \quad x = -3 \cdot \text{last } y = -6, \\ y = -4 \cdot \text{last } x = -8$$



The output of the state block's standard output is reset in t state port is the value standard output if the -Simulink Reference (2



Excerpt of C code produced by RTW (release R2009)

```
static void mdlOutputs(SimStruct * S, int_T tid)
{ _rtX = (ssGetContStates(S));
  ...
  _rtB = (_ssGetBlockIO(S));
  _rtB->B_0_0_0 = _rtX->Integrator1_CSTATE + _rtP->P_0;
  _rtB->B_0_1_0 = _rtP->P_1 * _rtX->Integrator1_CSTATE;
  if (ssIsMajorTimeStep (S))
  { ...
    if (zcEvent || ...)
      { (ssGetContStates (S))->Integrator0_CSTATE =
        _ssGetBlockIO (S)->B_0_1_0;
      }
    ...
    (_ssGetBlockIO (S))->B_0_2_0 =
      (ssGetContStates (S))->Integrator0_CSTATE;
    _rtB->B_0_3_0 = _rtP->P_2 * _rtX->Integrator0_CSTATE;
    if (ssIsMajorTimeStep (S))
    { ...
      if (zcEvent || ...)
        { (ssGetContStates (S))-> Integrator1_CSTATE =
          (ssGetBlockIO (S))->B_0_3_0;
        }
      ... } ... }
}
```

Before assignment:
integrator state
contains 'last' value


$$x = -3 \cdot \text{last } y$$

After assignment: integrator
state contains the new value


$$y = -4 \cdot x$$

So, y is updated with the new value of x

There is a problem in the treatment of causality.

Current Practice: conclusion

What is the semantics of these tools?

When the manual and implementations diverge, **which is right?**

There are **side effects**, **global variables**, **backtracking**.

Hard to judge whether the **generated code is correct**.

What more could we want?

An cleaner integration of discrete and continuous time.

Static rejection of bizarre programs.

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Interacting with a numerical solver

It is not always feasible, nor even possible, to calculate the behaviour of a hybrid model *analytically*.

All major tools thus calculate approximate solutions *numerically*.

Numerical solvers (e.g., LLNL Sundials CVODE)

Designed by experts!

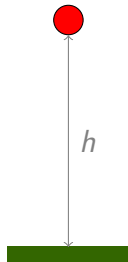
Compute a *discrete-time* approximations of *continuous-time* signals.

Subtle: variable step, change order dynamically, explicit/implicit.

Define compilation schemes with solver's internals kept *abstract*.

Bouncing ball

model



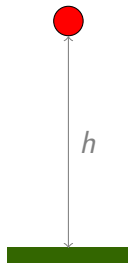
$$F = m \cdot a$$

$$m \cdot -g = m \cdot \frac{d^2 h(t)}{dt^2}$$

$$\frac{d^2 h(t)}{dt^2} = -g$$

Bouncing ball

model



$$F = m \cdot a$$

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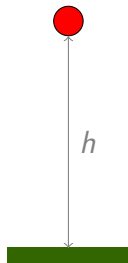
$$\dot{v} = -g \quad v(0) = v_0$$

$$\dot{h} = v \quad h(0) = h_0$$

First-order ODE

Bouncing ball

model



$$F = m \cdot a$$

$$m \cdot -g = m \cdot \frac{d^2 h(t)}{dt^2}$$

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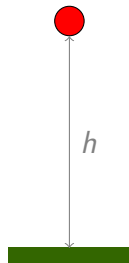
$$v(t) = v_0 + \int_0^t -g \cdot d\tau$$

$$h(t) = h_0 + \int_0^t v(\tau) \cdot d\tau$$

First-order ODE

Bouncing ball

model



$$F = m \cdot a$$

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$$\frac{d^2 h(t)}{dt^2} = -g$$

$$[\dot{v}; \dot{h}] = f(t, [v; h])$$

Solver

approximation

$$y_i = [v_0; h_0]$$

$$\begin{aligned} \dot{v} &= -g \\ \dot{h} &= v \end{aligned}$$

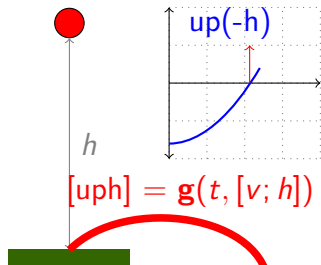
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First-order ODE

Bouncing ball

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$$F = m \cdot a$$

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$$[\dot{v}; \dot{h}] = f(t, [v; h])$$

Solver

event!

approximation

$$y_i = [v_0; h_0]$$

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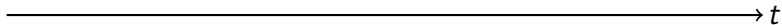
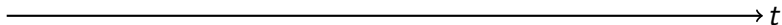
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First-order ODE

Solver execution (e.g., LLNL Sundials CVODE)

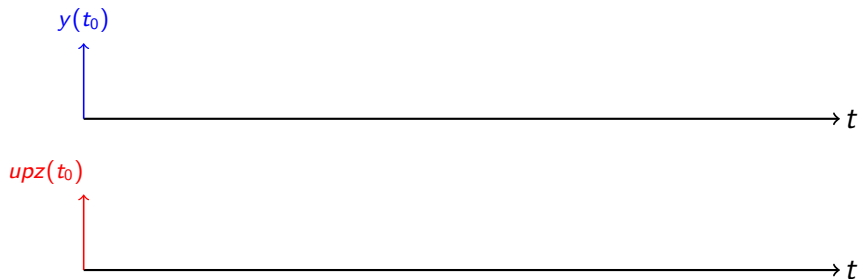
Give solver two functions: $\dot{y} = f_{\sigma}(t, y)$, $upz = g_{\sigma}(t, y)$



- Bigger and bigger steps (bound by h_{min} and h_{max})
- t does not necessarily advance monotonically
 - No side-effects within f or g

Solver execution (e.g., LLNL Sundials CVODE)

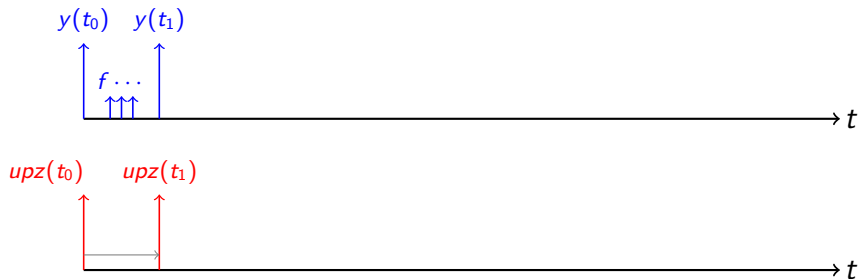
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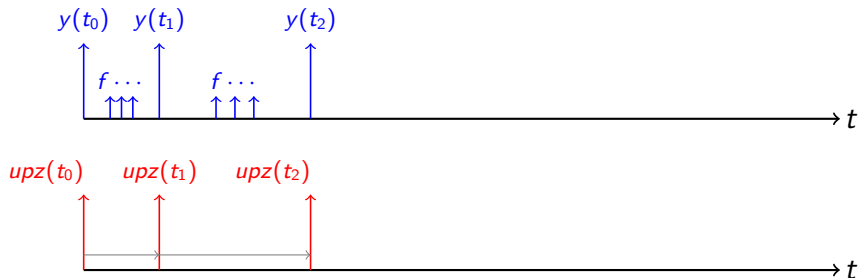
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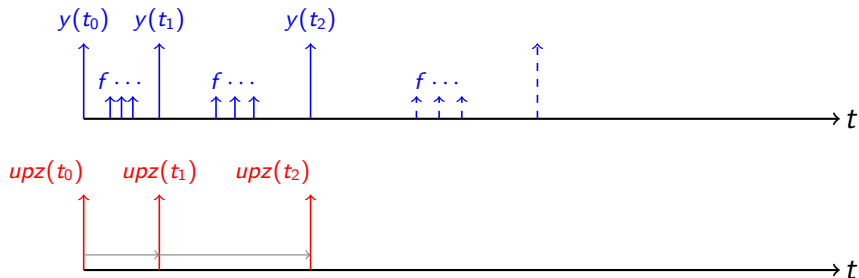


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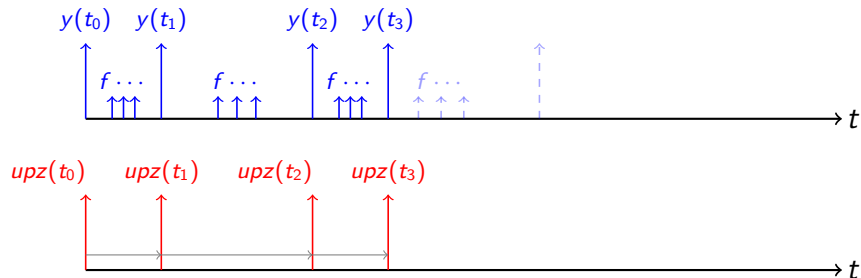
approximation error too large



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Solver execution (e.g., LLNL Sundials CVODE)

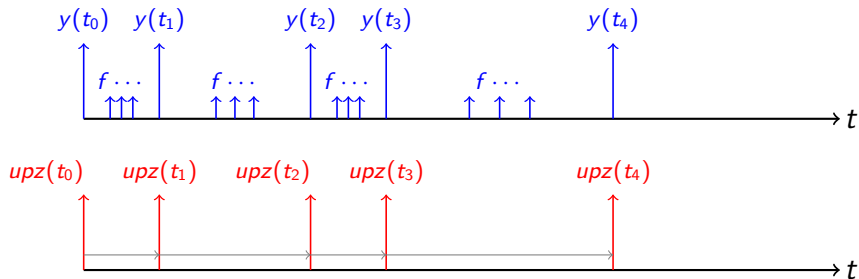
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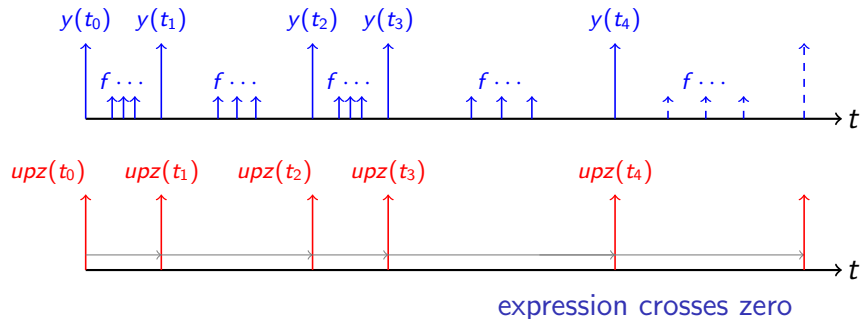
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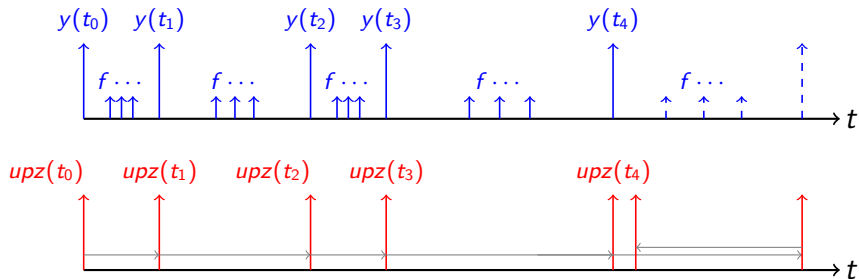
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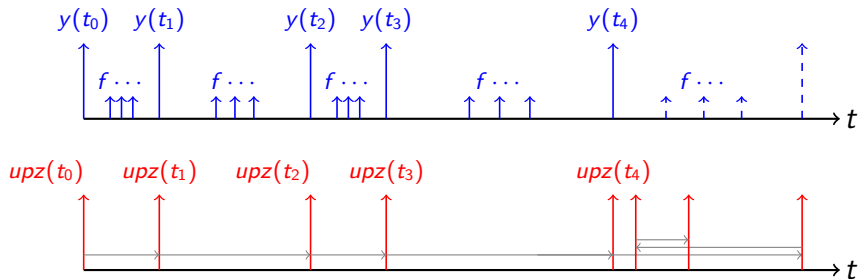
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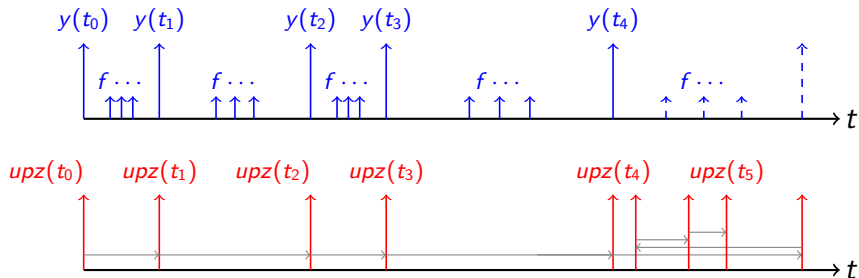
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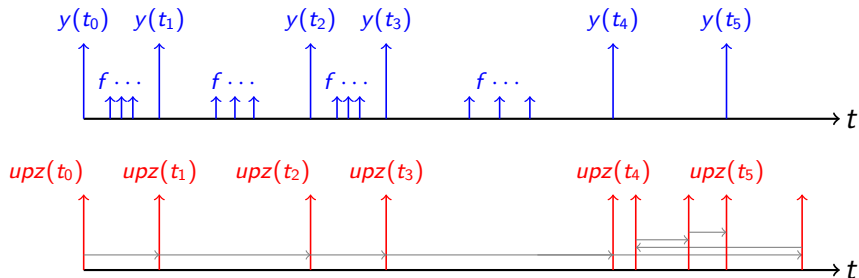
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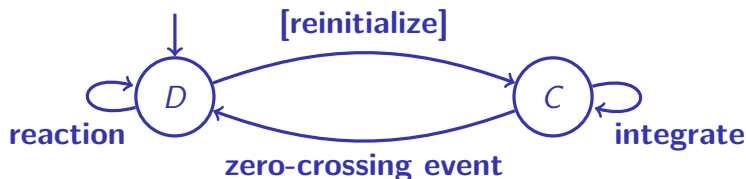
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The Simulation Engine of Hybrid Systems

Alternate discrete steps and integration steps

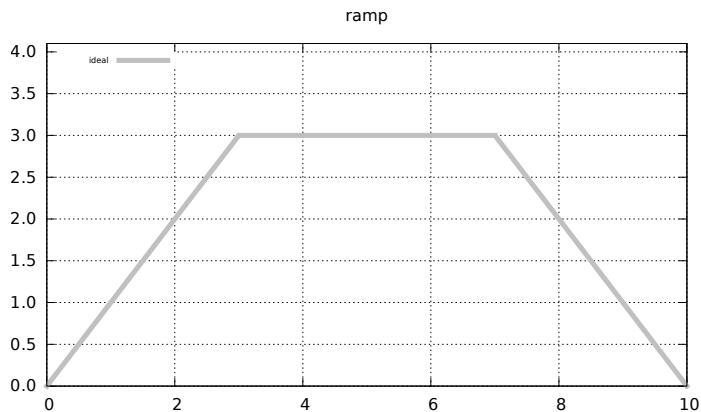


$$\sigma', y' = d_{\sigma}(t, y) \quad upz = g_{\sigma}(t, y) \quad \dot{y} = f_{\sigma}(t, y)$$

Properties of the three functions

- d_{σ} gathers all discrete changes.
- g_{σ} defines signals for zero-crossing detection.
- f_{σ} and g_{σ} should be free of side effects and, better, continuous.

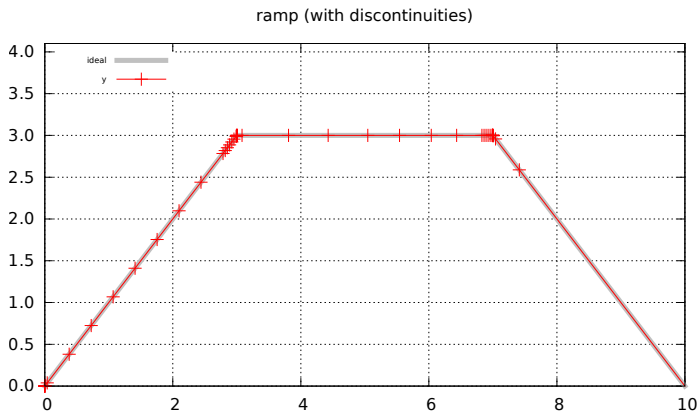
Numerical Integration (Sundials CVODE)



$$\dot{y}(t) = \begin{cases} 1 & \text{if } t < 3 \\ 0 & \text{if } 3 \leq t \leq 7 \\ -1 & \text{if } 7 < t \end{cases}$$

$$y(0) = 0$$

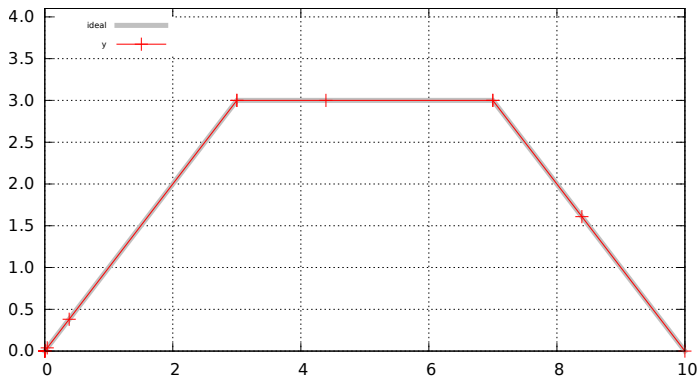
Numerical Integration: a derivative with a discontinuity



```
let f (t, y) =  
  if t < 3.0 then 1.0  
  else if t <= 7.0 then 0.0  
  else -1.0
```

Numerical Integration: discrete state with three modes

ramp (with zero-crossings and reinit)



```
let f (discrete_state) (t, y) =  
  match discrete_state with  
  | RampingUp → 1.0  
  | Flat → 0.0  
  | RampingDown → -1.0
```

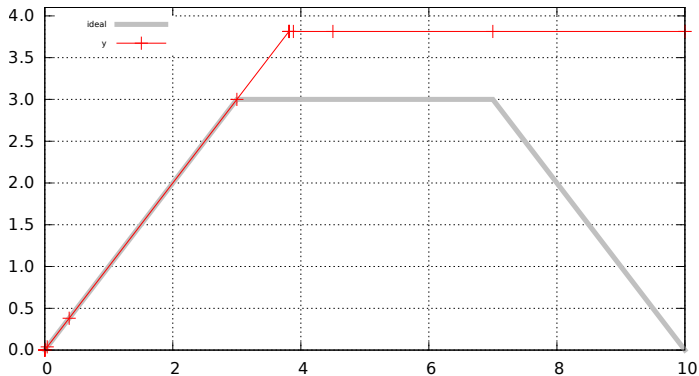
```
let g (discrete_state) (t, y) =  
  match discrete_state with  
  | RampingUp → t -. 3.0  
  | Flat → t -. 7.0  
  | _ → t
```

```
let discrete_state_i = RampingUp
```

```
let d (discrete_state) z (t, y) =  
  match discrete_state with  
  | RampingUp when z → Flat  
  | Flat when z → RampingDown  
  | s → s
```

Numerical Integration: with no reinit of the solver

ramp (with zero-crossings but no reinit)



Current Practice

Hybrid Systems Modelers

Some issues

Interlude: interacting with a numerical solver

Key elements of our approach

Non Standard Synchronous Semantics

Typing

Causality

Compilation

Key elements of our approach

Build a hybrid modeler on top of a **synchronous language**.

Use synchronous constructs for **arbitrary mix** of discrete and continuous.

Divide and Recycle

Recycle existing synchronous languages techniques.

Semantics, static checking, code-generation.

Divide from the code what is for the solver.

Simulate with off-the-shelf **numerical solvers**.

Be conservative: any synchronous program must be compiled, optimized, and executed as per usual.

These elements are experimented within the language Zélus.

Zélus

zelus.di.ens.fr

Combinatorial and sequential functions

A signal is a sequence of values. Nothing is said about the actual time to go from one instant to another.

```
let add (x,y) = x + y
```

```
let node min_max (x, y) = if x < y then (x, y) else (y, x)
```

```
let node after (n, t) = (c = n) where  
  rec c = 0 → pre(min(tick, n))  
  and tick = if t then c + 1 else c
```

When fed into the compiler, we get:

```
val add : int × int  $\xrightarrow{A}$  int
```

```
val min_max :  $\alpha \times \alpha \xrightarrow{D} \alpha \times \alpha$ 
```

```
val after : int × bool  $\xrightarrow{D}$  bool
```

x, y, etc. are infinite sequences of values.

The counter can be instantiated twice in a two state automaton,

```
let node blink (n, m, t) = x where
  automaton
  | On → do x = true  until (after(n, t)) then Off
  | Off → do x = false until (after(m, t)) then On
```

which returns a value for x that alternates between true for n occurrences of t and false for m occurrences of t .

```
let node blink_reset (r, n, m, t) = x where
  reset
  automaton
  | On → do x = true  until (after(n, t)) then Off
  | Off → do x = false until (after(m, t)) then On
  every r
```

The type signatures inferred by the compiler are:

```
val blink : int × int × bool  $\xrightarrow{D}$  bool
```

```
val blink_reset : bool × int × int × bool  $\xrightarrow{D}$  bool
```

Examples

Up to syntactic details, these are Scade 6 or Lucid Sychrone programs. E.g., a simple heat controller.⁷

```
(* an hysteresis controller for a heater *)
```

```
let hybrid heater(active) = temp where
```

```
  rec der temp = if active then c -. k *. temp else -. k *. temp init temp0
```

```
let hybrid hysteresis_controller(temp) = active where
```

```
  rec automaton
```

```
    | Idle → do active = false until (up(t_min -. temp)) then Active
```

```
    | Active → do active = true until (up(temp -. t_max)) then Idle
```

```
let hybrid main() = temp where
```

```
  rec active = hysteresis_controller(temp)
```

```
  and temp = heater(active)
```

⁷This is the hybrid version of one of Nicolas Halbwachs' examples with which he presented Lustre at the Collège de France, in January 2010.

The Bouncing ball [demo]

```
let hybrid bouncing(x0,y0,x'0,y'0) = (x,y) where
  rec
    der x = x' init x0
  and
    der x' = 0.0 init x'0
  and
    der y = y' init y0
  and
    der y' = -. g init y'0 reset up(-. y) → -0.9 *. last y'
```

Its type signature is:

```
val bouncing : float × float × float × float  $\xrightarrow{c}$  float × float
```

- When $-y$ crosses zero, re-initialize the speed y' with $-0.9 * \text{last } y'$.
- When y' is continuous, $\text{last } y'$ is the left limit of y' .
- As y' is immediately reset, writing $\text{last } y'$ is mandatory —otherwise, y' would instantaneously depend on itself.

Summary of Programming Constructs

- Synchronous constructs: data-flow equations/automata.
- An ODE with initial condition: `der x = e init e0`
- `last x` is the **left limit** of `x`.
- Detect a **zero-crossing** (from negative to positive): `up(x)`.
- This defines a **discrete instant**, that is, an **event**.
- All **discrete changes must occur on an event**. E.g.,:

```
let hybrid f(x, y) = (v, z1, z2) where
  rec v = present z1 → 1 | z2 → 2 init 0
  and z1 = up(x)
  and z2 = up(y)
```

```
val f : float × float  $\xrightarrow{c}$  int × zero × zero
```

- If `x = up(e)`, all handlers using `x` are governed by the same event.

Three difficulties

Semantics

- An ideal semantics to say which program make sense;
- useful to prove that compilation is correct.

Ensure that continuous and discrete time signals interfere correctly.

- Discrete time should stay logical and independent on when the solver decides to stop.
- Otherwise, we get the bizarre behaviors seen previously.

Ensure that fix-points exist and code can be scheduled.

- Algebraic loops must be statically detected.
- Restrict the use of `last x` so that signals are proved to be continuous during integration.

A Non-standard Semantics for Hybrid Modelers [JCSS'12]

We proposed to build the semantics on **non-standard analysis**.

`der y = z init 4.0 and z = 10.0 -. 0.1 *. y and k = y +. 1.0`

defines signals y , z and k , where for all $t \in \mathbb{R}^+$:

$$\frac{dy}{dt}(t) = z(t) \quad y(0) = 4.0 \quad z(t) = 10.0 - 0.1 \cdot y(t) \quad k(t) = y(t) + 1$$

What would be the value of y if it were computed by an ideal solver taking an **infinitesimal step of duration ∂** ?

${}^*y(n)$ stands for the values of y at instant $n\partial$, with $n \in {}^*\mathbb{N}$ a non-standard integer.

$${}^*y(0) = 4$$

$${}^*y(n+1) = {}^*y(n) + {}^*z(n) \cdot \partial$$

$${}^*z(n) = 10 - 0.1 \cdot {}^*y(n)$$

$${}^*k(n) = {}^*y(n) + 1$$

Non standard semantics [JCSS'12]

Let ${}^*\mathbb{R}$ and ${}^*\mathbb{N}$ be the non-standard extensions of \mathbb{R} and \mathbb{N} .

Let $\partial \in {}^*\mathbb{R}$ be an infinitesimal, i.e., $\partial > 0, \partial \approx 0$.

Let the global time base or **base clock** be the infinite set of instants:

$$\mathbb{T}_\partial = \{t_n = n\partial \mid n \in {}^*\mathbb{N}\}$$

\mathbb{T}_∂ inherits its total order from ${}^*\mathbb{N}$. A sub-clock $T \subset \mathbb{T}_\partial$.

What is a discrete clock?

*A clock T is termed **discrete** if it is the result of a zero-crossing or a sub-sampling of a discrete clock. Otherwise, it is termed **continuous**.*

If $T \subseteq \mathbb{T}$, we write $\bullet T(t)$ for the immediate predecessor of t in T and $T^\bullet(t)$ for the immediate successor of t in T .

A signal is a partial function from \mathbb{T} to a set of values.

Semantics of basic operations

Replay the classical semantics of a synchronous language.

An ODE with reset on clock T : $\text{der } x = e \text{ init } e_0 \text{ reset } z \longrightarrow e_1$

$$*x(t_0) = *e_0(0) \text{ if } t_0 = \min T$$

$$*x(t) = \text{if } *z(t) \text{ then } *e_1(t) \text{ else } *x(\bullet T(t)) + \partial \cdot *e(\bullet T(t)) \text{ if } t \in T$$

last x if x is defined on clock T

$$*\text{last } x(t) = *x(\bullet T(t))$$

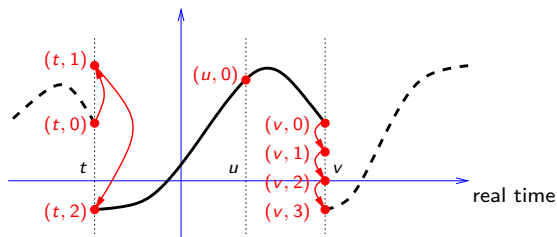
Zero-crossing $\text{up}(x)$ on clock T

$$*\text{up}(x)(t_0) = \text{false} \text{ if } t_0 = \min T$$

$$*\text{up}(x)(t) = (*x(\bullet T(t)) \leq 0) \wedge (*x(t) > 0) \text{ if } t \in T$$

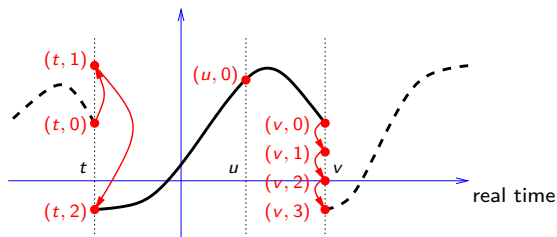
Non-standard time vs. Super-dense time

- Maler et al., Lee et al. super-dense time modeling $\mathbb{R} \times \mathbb{N}$

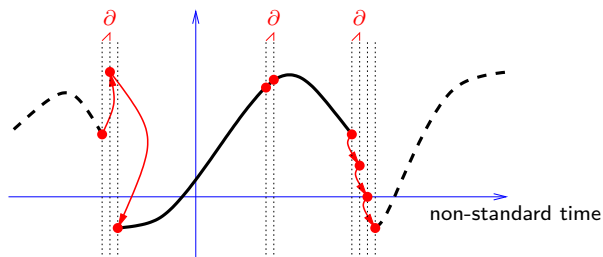


Non-standard time vs. Super-dense time

- Edward Lee & al. super-dense time modeling $\mathbb{R} \times \mathbb{N}$



- non-standard time modeling $\mathbb{T}_\partial = \{n\partial \mid n \in \mathbb{N}\}$



Typing: mixing discrete (logical) time and continuous time

The following two parallel composition make sense.

Discrete time: the clock should be discrete

```
let node sum(x) = cpt where
  rec cpt = 0 → pcpt
  and pcpt = pre(cpt) + x
```

Continuous time: the clock should be continuous

```
let hybrid bouncing(y0, y'0) = o where
  rec der y = y' init y0
  and der y' = -.g init y'0
  and o = y +. 10.0
```

The following do not make sense

At what clock should we compute cpt?

```
rec der t = 1.0 init 0.0
and cpt = 0.0 → pre(cpt) + t
```

Intuition

Distinguish functions with three kinds **A/D/C**.

- Combinatorial function get kind **A** (for “any”).
- Discrete-time (synchronous) functions get kind **D** (for “discrete”).
- Continuous-time (hybrid) functions get kind **C** (for “continuous”).

Explicitly relate simulation and logical time

All discontinuities and side effects must be aligned with a zero-crossing instant.

```
let hybrid correct (z) = (time, y) where
  rec der time = 1.0 init 0.0
  and y = present up(z) → sum(time) init 0.0
```

Basic typing [LCTES'11]

A simple ML type system with effects.

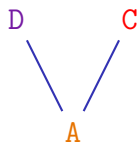
The type language

$bt ::= \text{float} \mid \text{int} \mid \text{bool} \mid \text{zero}$

$t ::= bt \mid t \times t \mid \beta$

$\sigma ::= \forall \beta_1, \dots, \beta_n. t \xrightarrow{k} t$

$k ::= \mathbf{D} \mid \mathbf{C} \mid \mathbf{A}$



Initial conditions

$(+)$: $\text{int} \times \text{int} \xrightarrow{\mathbf{A}} \text{int}$

if : $\forall \beta. \text{bool} \times \beta \times \beta \xrightarrow{\mathbf{A}} \beta$

$(=)$: $\forall \beta. \beta \times \beta \xrightarrow{\mathbf{D}} \text{bool}$

$\text{pre}(\cdot)$: $\forall \beta. \beta \xrightarrow{\mathbf{D}} \beta$

$\cdot \text{fby} \cdot$: $\forall \beta. \beta \times \beta \xrightarrow{\mathbf{D}} \beta$

$\text{up}(\cdot)$: $\text{float} \xrightarrow{\mathbf{C}} \text{zero}$

What about continuous automata? [EMSOFT'11]

Stateflow User's Guide The Mathworks, pages 16-26 to 16-29, 2011.

16 Modeling Continuous-Time Systems in Stateflow[®] Charts

Design Considerations for Continuous-Time Modeling in Stateflow[®] Charts

16 Modeling Continuous-Time Systems in Stateflow[®] Charts

Design Considerations for Continuous-Time Modeling in Stateflow Charts

In this section...

"Rationale for Design Considerations" on page 16-26

"Summary of Rules for Continuous-Time Modeling" on page 16-26

Rationale for Design Considerations

To guarantee the integrity — or, sometimes — of the results in continuous-time modeling, you must constrain your charts to a restricted subset of Stateflow chart semantics. The restricted semantics ensure that inputs do not depend on unpredictable factors — or side effects — such as:

- Simulink solver's guess for number of minor intervals in a major time step
- Number of iterations required to stabilize the integration loop or zero crossing loop

By minimizing side effects, a Stateflow chart can maintain its state at minor time steps and, therefore, update state only during major time steps when state changes occur. Using this heuristic, a Stateflow chart can always compute outputs based on a constant state for continuous time.

A Stateflow chart generates informative errors to help you correct semantic violations.

Summary of Rules for Continuous-Time Modeling

How are the rules for modeling continuous-time Stateflow charts:

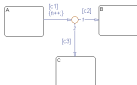
Update local data only in transition, entry, and exit actions

To maintain precision in continuous-time simulation, you should update local data (continuous or discrete) only during physical events at major time steps.

In Stateflow charts, physical events cause state transitions. Therefore, write to local data only in actions that execute during transitions, as follows:

- State exit actions, which execute before leaving the state at the beginning of the transition.
- Transition actions, which execute during the transition.
- State entry actions, which execute after entering the new state at the end of the transition.
- Condition actions on a transition, but only if the transition directly reaches a state.

Consider the following chart:



In this example, the action `[i = i + 1]` executes even when conditions C2 and C3 are false. In this case, it gets updated in a minor time step because there is no state transition.

Do not write to local continuous data in *startUp* actions because those actions execute in minor time steps.

Do not call Simulink functions in state during actions or transition conditions

This rule applies to continuous-time charts because you cannot call functions during minor time steps. You can call Simulink functions in state *entry* or *exit* actions and transition actions. However, if you try to call Simulink

functions in state *during* actions or transition conditions, an error message appears when you simulate your model.

For more information, see Chapter 21, "Using Simulink Functions in Stateflow Charts".

Compute derivatives only in *during* actions

A Simulink model needs continuous-time derivatives during minor time steps. The only part of a Stateflow chart that executes during minor time steps is the *during* action. Therefore, you should compute derivatives in *during* actions to give your Simulink model the most recent calculation.

Do not read outputs and derivatives in states or transitions

This restriction ensures smooth outputs in a major time step because it prevents a Stateflow chart from using values that may no longer be valid in the current minor time step. Instead, a Stateflow chart always computes outputs from local discrete data, local continuous data, and chart inputs.

Use discrete variables to govern conditions in *during* actions

This restriction prevents state changes from occurring between major time steps. When placed in *during* actions, conditions that affect control flow should be governed by discrete variables because they do not change between major time steps.

Do not use input events in continuous-time charts

The presence of input events makes a chart behave like a triggered subsystem and therefore unable to simulate in continuous time. For example, the following model generates an error if the chart uses a continuous update method.

16-26

16-27

16-28

- 'Restricted subset of Stateflow chart semantics'
 - restricts side-effects to major time steps
 - supported by warnings and errors in tool (**mostly**)

What about continuous automata? [EMSOFT'11]

Stateflow User's Guide The Mathworks, pages 16-26 to 16-29, 2011.

16 Modeling Continuous-Time Systems in Stateflow® Charts

Design Considerations for Continuous-Time Modeling in Stateflow® Charts

16 Modeling Continuous-Time Systems in Stateflow® Charts

Design Considerations for Continuous-Time Modeling in Stateflow Charts

In this section...

'Update local data *only* in transition, entry, and exit actions'

Rationale for Design Considerations

To guarantee the integrity — or semantics — of the results in continuous-time modeling, you must constrain your charts to a restricted subset of Stateflow chart semantics. The restricted semantics ensure that inputs do not depend on unpredictable factors — or side effects — such as:

- Stateflow solver's guess for number of minor intervals in a major time step
- Number of iterations required to stabilize the integration loop or zero crossing loop

By minimizing side effects, a Stateflow chart can maintain its state at minor time steps and, therefore, update state only during major time steps when made-changes occur. Using this heuristic, a Stateflow chart can always compute outputs based on a constant state for continuous time.

A Stateflow chart generates informative errors to help you correct semantic violations.

Summary of Rules for Continuous-Time Modeling

How are the rules for modeling continuous-time Stateflow charts?

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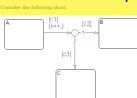
- State exit actions, which execute before leaving the state at the beginning of the transition.

- Transition actions, which execute during the transition.

- Entry actions, which execute after entering the new state at the beginning of the transition.

- Exit actions, which execute before leaving the new state at the beginning of the transition.

Consider the following chart:



In this example, the action [x+1] executes even when conditions C2 and C3 are false. In this case, it gets updated in a minor time step because there is no state transition.

Do not write to local continuous data in during actions because those actions execute in minor time steps.

Do not call Simulink functions in state during actions or transition conditions.

This rule applies to continuous-time charts because you cannot call Simulink functions during minor time steps. You can call Simulink functions in state entry or exit actions and transition actions. However, if you try to call Simulink

functions in state during actions or transition conditions, an error message appears when you simulate your model.

For more information, see Chapter 24, "Using Simulink Functions in Stateflow Charts."

The restriction prevents made changes from occurring between major time steps. When placed in during actions, conditions that affect control flow should be governed by discrete variables because they do not change between major time steps.

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Do not read outputs and derivatives in states or transitions

This restriction ensures smooth outputs in a major time step because it prevents a Stateflow chart from using values that may no longer be valid in the current minor time step. Instead, a Stateflow chart always computes outputs from local discrete data, local continuous data, and chart inputs.

Use discrete variables to govern conditions in during actions

This restriction prevents made changes from occurring between major time steps. When placed in during actions, conditions that affect control flow should be governed by discrete variables because they do not change between major time steps.

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The presence of input events makes a chart behave like a triggered subsystem and therefore unable to simulate in continuous time. For example, the following model generates an error if the chart uses a continuous update method.

16-26

16-27

16-28

- 'Restricted subset of Stateflow chart semantics'
 - restricts side-effects to major time steps
 - supported by warnings and errors in tool (**mostly**)

What about continuous automata? [EMSOFT'11]

Stateflow User's Guide The Mathworks, pages 16-26 to 16-29, 2011.

The image shows a screenshot of the Stateflow User's Guide, pages 16-26 to 16-29. The page is divided into three columns. The left column (page 16-26) contains sections: 'Design Considerations for Continuous-Time Modeling in Stateflow Charts', 'Summary of Rules for Continuous-Time Modeling', and 'Update local data only in transition, entry, and exit actions'. The middle column (page 16-27) contains a diagram of a Stateflow chart with two states, A and B, and a transition between them. The right column (page 16-28) contains sections: 'Do not use outputs and derivatives in states or transitions', 'Do not use input events in continuous-time charts', and 'Compute derivatives only in during actions'. Three yellow highlight boxes are overlaid on the page. The first box, spanning the top of the left and middle columns, contains the text: 'Update local data *only* in transition, entry, and exit actions'. The second box, spanning the middle and right columns, contains the text: 'Do not call Simulink functions in state during actions or transition conditions'. The third box, located in the middle column below the diagram, contains the text: 'Compute derivatives only in during actions'. The page number '16-26' is visible in the bottom left corner, and '16-27' and '16-28' are visible in the bottom right corner of the respective columns.

- 'Restricted subset of Stateflow chart semantics'
 - restricts side-effects to major time steps
 - supported by warnings and errors in tool (**mostly**)

Causality loops

Yet, some programs are well typed but have algebraic loops.

Which programs should we accept?

- OK to reject (no solution).

```
rec x = x + 1
```

- OK as an algebraic constraint (e.g., Simulink and Modelica).

```
rec x = 1 - x
```

- But NOK if sequential code generation is targeted.

- `last` `x` does not necessarily break causality loops!

```
rec x = last x + 1
```

- OK

```
rec der x = 1.0 init 0.0 reset z → t
```

```
and y = x +. 1.0
```

```
and t = last y
```

Can we find a simple and uniform justification?

ODEs with reset

Consider the sawtooth signal $y : \mathbb{R}^+ \mapsto \mathbb{R}^+$ such that:

$$\frac{dy}{dt}(t) = 1 \quad y(t) = 0 \text{ if } t \in \mathbb{N}$$

written:

```
der y = 1.0 init 0.0 reset up(y -. 1.0) → 0.0
```

The ideal non-standard semantics is:

$$\begin{aligned} {}^*y(0) &= 0 & {}^*y(n) &= \text{if } {}^*z(n) \text{ then } 0.0 \text{ else } {}^*ly(n) \\ {}^*ly(n) &= {}^*y(n-1) + \partial & {}^*c(n) &= ({}^*y(n) - 1) \geq 0 \\ {}^*z(0) &= \text{false} & {}^*z(n) &= {}^*c(n) \wedge \neg {}^*c(n-1) \end{aligned}$$

This set of equation is not causal: ${}^*y(n)$ depends on itself.

Accessing the “left limit” of a signal

There are two ways to break this cycle:

- consider that the effect of the zero-crossing is delayed by one cycle, that is, the test is made on $*z(n-1)$ instead of on $z(n)$, or,
- distinguish the current value of $*y(n)$ from the value it would have had were there no reset, namely $*ly(n)$.

Testing a zero-crossing of ly (instead of y),

$$*c(n) = (*ly(n) - 1) \geq 0,$$

gives a program that is causal since $*y(n)$ no longer depends instantaneously on itself.

```
der y = 1.0 init 0.0 reset up(last y -. 1.0) → 0.0
```

An explanation of the bug

The source program

```
rec der x = 1.0 init 0.0 reset z → -3.0 *. last y
and der y = x init 0.0 reset z → -4.0 *. last x
and z = up(last x -. 2.0)
```

Its non-standard interpretation

$$\begin{aligned} *x(n) &= \text{if } *z(n) \text{ then } -3 \cdot *y(n-1) \text{ else } *x(n-1) + \partial \\ *y(n) &= \text{if } *z(n) \text{ then } -4 \cdot *x(n-1) \text{ else } *y(n-1) + \partial \cdot *x(n-1) \\ &\dots \end{aligned}$$

Explanation

- The first two equations are scheduled this way so $*x(n-1)$ is lost.
- This is a scheduling bug: the sequential code lacks a copy variable.

Causality Analysis [HSCC'14]

Every feedback loop must cross a delay.

Intuition: associate a time stamp to every expression and ensure that the relation between those time stamps is a partial order.

The type language

$$\begin{aligned}\sigma & ::= \forall \alpha_1, \dots, \alpha_n : C. ct \xrightarrow{k} ct \\ ct & ::= ct \times ct \mid \alpha \\ k & ::= D \mid C \mid A\end{aligned}$$

Precedence relation:

$$C ::= \{\alpha_1 < \alpha'_1, \dots, \alpha_n < \alpha'_n\}$$

$<$ must be a strict partial order. $C \vdash ct_1 < ct_2$ means that ct_1 precedes ct_2 according to C .

Associate a type that express input/output dependences. E.g.,

```
let node plus(x, y) = x + 0 → pre y
```

We get: $f : \forall \alpha_1, \alpha_2. \alpha_1 \times \alpha_2 \xrightarrow{D} \alpha_1$

- `der x` breaks a loop: `der temp = c -. temp init 20.0` is correct.
- `last(x)` breaks a loop in a discrete context.

The following is rejected; the next is accepted.

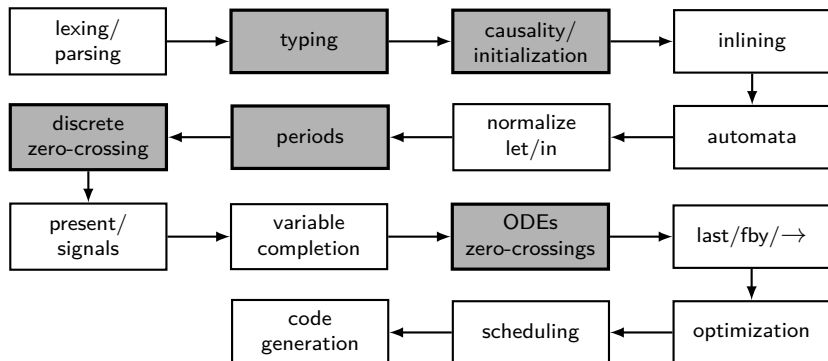
```
rec der y' = -. g init 0.0 reset up(-.y) → -0.9 *. y'  
and der y = y' init y0
```

```
rec der y' = -. g init 0.0 reset up(-.y) → -0.9 *. last y'  
and der y = y' init y0
```

Major theorem: [HSCC 14] Well typed programs define continuous signals during integration.

The proof deeply rely on the use of the non-standard synchronous semantics.

Compiler architecture



Built on an existing synchronous compiler

- Source-to-source and traceable transformations
- Resulting program is synchronous and translated to sequential code

Comparison with existing tools

Simulink/Stateflow (Mathworks)

- Integrated treatment of automata vs two distinct languages
- More rigid separation of discrete and continuous behaviors

Modelica

- Do not handle DAEs
- Our proposal for automata has been integrated into version 3.3

Ptolemy (E.A. Lee et al., Berkeley)

- A unique computational model: synchronous
- Everything is compiled to sequential code (not interpreted)