Traces Properties
Semantics and applications to verification

Xavier Rival
École Normale Supérieure

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Prove that $[P] \subseteq S$ (i.e., all behaviors of $P$ satisfy specification $S$)
where $[P]$ is the program semantics and $S$ the desired specification

Last week, we studied a form of $[P]$...

Today’s lecture: we look back at program’s properties

- families of properties:
  what properties can be considered “similar” ? in what sense ?

- proof techniques:
  how can those kinds of properties be established ?

- specification of properties:
  are there languages to describe properties ?
A high level overview

- In this lecture we look at **trace properties**
- A property is a set of traces, defining the admissible executions

**Safety properties:**
- something (e.g., bad) will never happen
- proof by invariance

**Liveness properties:**
- something (e.g., good) will eventually happen
- proof by variance

Some interesting program properties do not fit in this classification
State properties

As usual, we consider $S = (S, \rightarrow, S_I)$

First approach: properties as sets of states

- A property $\mathcal{P}$ is a set of states $\mathcal{P} \subseteq S$
- $\mathcal{P}$ is satisfied if and only if all reachable states belong to $\mathcal{P}$, i.e., $[S]_R \subseteq \mathcal{P}$ where $[S]_R = \{s_n \in S | \exists \langle s_0, \ldots, s_n \rangle \in [S]^*, s_0 \in S_I\}$

Examples:

- Absence of runtime errors:
  
  $\mathcal{P} = S \setminus \{\Omega\}$ where $\Omega$ is the error state

- Non termination (e.g., for an operating system):
  
  $\mathcal{P} = \{s \in S | \exists s' \in S, s \rightarrow s'\}$
Second approach: properties as sets of traces

- A property $\mathcal{T}$ is a set of traces $\mathcal{T} \subseteq S^\infty$
- $\mathcal{T}$ is satisfied if and only if all traces belong to $\mathcal{T}$, i.e., $[S]^\infty \subseteq \mathcal{T}$

Examples:

- Obviously, state properties are trace properties
- Functional properties:
  e.g., “program $P$ takes one integer input $x$ and returns its absolute value”
- Termination: $\mathcal{T} = S^*$ (i.e., the system should have no infinite execution)
Monotonicity

**Property 1**

Let $P_0, P_1 \subseteq S$ be two state properties, such that $P_0 \subseteq P_1$. Then $P_0$ is stronger than $P_1$, i.e. if program $S$ satisfies $P_0$, then it also satisfies $P_1$.

**Property 2**

Let $T_0, T_1 \subseteq S$ be two trace properties, such that $T_0 \subseteq T_1$. Then $T_0$ is stronger than $T_1$, i.e. if program $S$ satisfies $T_0$, then it also satisfies $T_1$.

**Proofs:**

straightforward application of the definition of state (resp., trace) properties
Outline

1. Safety properties
   - Informal and formal definitions
   - Proof method

2. Liveness properties

3. Decomposition of trace properties

4. A Specification Language: Temporal logic

5. Beyond safety and liveness

6. Conclusion
Safety properties

Informal definition: safety properties

A safety property is a property which specifies that some (bad) behavior will never occur

- **Absence of runtime errors** is a safety property ("bad thing": error)
- **State properties** is a safety property ("bad thing": reaching $S \setminus P$)
- **Non termination** is a safety property ("bad thing": reaching a blocking state)
- "**Not reaching state $b$ after visiting state $a$**" is a safety property (and not a state property)
- **Termination** is not a safety property

We now intend to provide a **formal definition** of safety.
Towards a formal definition

How to refute a safety property?

- We assume $S$ does not satisfy safety property $\mathcal{P}$
- Thus, there exists a counter-example trace $\sigma = \langle s_0, \ldots, s_n, \ldots \rangle \in \mathcal{F}[S] \setminus \mathcal{P}$
  it may be finite or infinite...
- The intuitive definition says this trace eventually exhibits some bad behavior
- Thus, there exists a rank $i \in \mathbb{N}$, such that the bad behavior has been observed before reaching $s_i$
- Therefore, trace $\sigma' = \langle s_0, \ldots, s_i \rangle$ violates $\mathcal{P}$, i.e. $\sigma' \notin \mathcal{P}$
- We remark $\sigma'$ is finite

A safety property that does not hold can always be refuted with a finite counter-example
Safety properties
Informal and formal definitions

Limit

**Definition: upper closure operator (uco)**

Function \( \phi : S \rightarrow S \) is an upper closure operator iff:

- **monotone**
- **extensive:** \( \forall x \in S, \ x \subseteq \phi(x) \)
- **idempotent:** \( \forall x \in S, \ \phi(\phi(x)) = \phi(x) \)

**Definition: limit**

The limit operator is defined by:

\[
\text{Lim} : \mathcal{P}(S^\infty) \rightarrow \mathcal{P}(S^\infty)
\]

\[
X \mapsto X \cup \{ \sigma \in S^\infty | \forall i \in \mathbb{N}, \ \sigma|_i \in X \}
\]

Operator \( \text{Lim} \) is an upper-closure operator

**Proof:** exercise!
Prefix closure

We write \( \sigma_{\lfloor i \rfloor} \) for the prefix of length \( i \) of trace \( \sigma \):

\[
\begin{align*}
\langle s_0, \ldots, s_n \rangle_{\lfloor 0 \rfloor} &= \epsilon \\
\langle s_0, \ldots, s_n \rangle_{\lfloor i+1 \rfloor} &= \begin{cases} 
\langle s_0, \ldots, s_i \rangle & \text{if } i < n \\
\langle s_0, \ldots, s_n \rangle & \text{otherwise}
\end{cases} \\
\langle s_0, \ldots \rangle_{\lfloor i+1 \rfloor} &= \langle s_0, \ldots, s_i \rangle
\end{align*}
\]

If \( \sigma \) is finite, of length \( n \), \( |\sigma|_i = \min(n, i) \); if \( \sigma \) is infinite, \( |\sigma|_i = i \).

**Definition: prefix closure**

The prefix closure operator is defined by:

\[
\text{PCI} : \quad \mathcal{P}(S^\infty) \longrightarrow \mathcal{P}(S^*) \quad X \longmapsto \{ \sigma_{\lfloor i \rfloor} \mid \sigma \in X, i \in \mathbb{N} \}
\]

**Properties:**

- PCI is monotone
- PCI is idempotent, i.e., \( \text{PCI} \circ \text{PCI}(X) = \text{PCI}(X) \)
Safety properties: formal definition

An upper closure operator

Operator \textit{Safe} is defined by \textit{Safe} = \textit{Lim} \circ \textit{PCl}.
It is an upper closure operator over $\mathcal{P}(S^\infty)$

Proof:

\textit{Safe} is monotone since \textit{Lim} and \textit{PCl} are monotone

\textit{Safe} is extensive:
indeed if $X \subseteq S^\infty$ and $\sigma \in X$, we can show that $\sigma \in \textit{Safe}(X)$:

- if $\sigma$ is a finite trace, it is one of its prefixes, so $\sigma \in \textit{PCl}(X) \subseteq \textit{Lim}(\textit{PCl}(X))$
- if $\sigma$ is an infinite trace, all its prefixes belong to $\textit{PCl}(X)$, so $\sigma \in \textit{Lim}(\textit{PCl}(X))$
Safety properties: formal definition

**Proof** (continued):

**Safe is idempotent:**
- as **Safe** is extensive and monotone **Safe** ⊆ **Safe** ∘ **Safe**, so we simply need to show that **Safe** ∘ **Safe** ⊆ **Safe**
- let \( X \subseteq S^\infty \), \( \sigma \in \text{Safe}(\text{Safe}(X)) \); then:
  \[
  \sigma \in \text{Safe}(\text{Safe}(X))
  \]
  \[\Rightarrow \forall i, \sigma_{[i]} \in \text{PCI} \circ \text{Safe}(X) \text{ by def. of Lim}\]
  \[\Rightarrow \forall i, \exists \sigma', j, \sigma_{[i]} = \sigma'_{[j]} \land \sigma' \in \text{Safe}(X) \text{ by def. of PCI}\]
  \[\Rightarrow \forall i, \exists \sigma', j, \sigma_{[i]} = \sigma'_{[j]} \land \forall k, \sigma'_{[k]} \in \text{PCI}(X) \text{ by def. of Lim}\]
  \[\Rightarrow \forall i, \exists \sigma', j, \sigma_{[i]} = \sigma'_{[j]} \land \sigma'_{[i]} \in \text{PCI}(X) \text{ with } i = j\]

- if \( \sigma \) is finite, we let \( i = |\sigma| \), thus \( j \) has to be equal to \( n \) as well and \( \sigma = \sigma'_{[i]} \in \text{PCI}(X) \), thus \( \sigma \in \text{Lim}(\text{PCI}(X)) \)
- if \( \sigma \) is infinite, \( |\sigma_{[i]}| = i \) and we may let \( i = k \) so
  \[\forall i, \sigma_{[i]} = \sigma'_{[i]} \in \text{PCI}(X)\]
  thus \( \sigma \in \text{Lim}(\text{PCI}(X)) \)
Safety properties: formal definition

Safety: definition
A trace property $\mathcal{T}$ is a safety property if and only if $\text{Safe}(\mathcal{T}) = \mathcal{T}$

Theorem
If $\mathcal{T}$ is a trace property, then $\text{Safe}(\mathcal{T})$ is a safety property

Proof:
Straightforward, by idempotence of Safe
Example

We assume that:

- $S = \{a, b\}$
- $T$ states that \textit{a should not be visited after state $b$ is visited};
  elements of $T$ are of the general form
  \[
  \langle a, a, a, \ldots, a, b, b, b, \ldots \rangle \text{ or } \langle a, a, a, \ldots, a, a, \ldots \rangle
  \]

Then:

- $\text{PCI}(T)$ elements are all finite traces which are of the above form (i.e., made of $n$ occurrences of $a$ followed by $m$ occurrences of $b$, where $n, m$ are positive integers)
- $\text{Lim}(\text{PCI}(T))$ adds to this set the trace made made of infinitely many occurrences of $a$ and the infinite traces made of $n$ occurrences of $a$ followed by infinitely many occurrences of $b$
- thus, $\text{Safe}(T) = \text{Lim}(\text{PCI}(T)) = T$

Therefore $T$ is indeed formally \textbf{a safety property}. 
State properties are safety properties

Theorem

Any state property is also a safety property.

Proof:

Let us consider state property $\mathcal{P}$.
It is equivalent to trace property $\mathcal{T} = \mathcal{P}^\infty$:

$$
\text{Safe}(\mathcal{T}) = \text{Lim}(\text{PCI}(\mathcal{P}^\infty))
\steq
\text{Lim}(\mathcal{P}^*)
\steq
\mathcal{P}^* \cup \mathcal{P}^\omega
\steq
\mathcal{P}^\infty
\steq
\mathcal{T}
$$

Therefore $\mathcal{T}$ is indeed a safety property.
Intuition of the formal definition

Operator **Safe saturates** a set of traces $S$ with
- prefixes
- infinite traces all finite prefixes of which can be observed in $S$

Thus, if $\text{Safe}(S) = S$ and $\sigma$ is a trace, to establish that $\sigma$ is not in $S$, it is sufficient to discover a **finite prefix of $\sigma$** that cannot be observed in $S$.

Alternatively, if all finite prefixes of $\sigma$ belong to $S$ or can observed as a prefix of another trace in $S$, by definition of the limit operator, $\sigma$ **belongs to $S$** (even if it is infinite).

Thus, our definition **indeed captures properties that can be disproved with a counter-example**.
### Outline

1. **Safety properties**
   - Informal and formal definitions
   - Proof method

2. **Liveness properties**

3. **Decomposition of trace properties**

4. **A Specification Language: Temporal logic**

5. **Beyond safety and liveness**

6. **Conclusion**
Proof by invariance

- We consider transition system $\mathcal{S} = (\mathcal{S}, \rightarrow, \mathcal{S}_I)$, and safety property $\mathcal{T}$. Finite traces semantics is the least fixpoint of $F_*$. 
- We seek a way of verifying that $\mathcal{S}$ satisfies $\mathcal{T}$, i.e., that $\mathcal{I}_S \subseteq \mathcal{T}$.

**Principle of invariance proofs**

Let $\mathcal{I}$ be a set of finite traces; it is said to be an invariant if and only if:
- $\forall s \in \mathcal{S}_I, \langle s \rangle \in \mathcal{I}$
- $F_*(\mathcal{I}) \subseteq \mathcal{I}$

It is stronger than $\mathcal{T}$ if and only if $\mathcal{I} \subseteq \mathcal{T}$.

The “by invariance” proof method is based on finding an invariant that is stronger than $\mathcal{T}$. 
Soundness

**Theorem: soundness**

The invariance proof method is **sound**: if we can find an invariant for $S$, that is stronger than $T$, then $S$ satisfies $T$.

**Proof:**

We assume that $I$ is an invariant of $S$ and that it is stronger than $T$, and we show that $S$ satisfies $T$:

- by induction over $n$, we can prove that $F_n^*(\{\langle s \rangle \mid s \in S_I \}) \subseteq F_n^*(I) \subseteq I$
- therefore $[S]^* \subseteq I$
- thus, $\text{Safe}([S]^*) \subseteq \text{Safe}(I) \subseteq \text{Safe}(T)$ since $\text{Safe}$ is monotone
- we remark that $[S]^{\infty} = \text{Safe}([S]^*)$
- $T$ is a safety property so $\text{Safe}(T) = T$
- we conclude $[S]^{\infty} \subseteq T$, i.e., $S$ satisfies property $T$
Completeness

**Theorem: completeness**

The invariance proof method is complete: if $S$ satisfies $\mathcal{T}$, then we can find an invariant $I$ for $S$, that is stronger than $\mathcal{T}$.

**Proof:**
We assume that $[S]^\infty$ satisfies $\mathcal{T}$, and show that we can exhibit an invariant.

Then, $I = [S]^\infty$ is an invariant of $S$ by definition of $[.]^\infty$, and it is stronger than $\mathcal{T}$.

**Caveat:**
- $[S]^\infty$ is most likely not a very easy to express invariant
- it is just a convenient completeness argument
- so, completeness does not mean the proof is easy!
We consider the proof that the program below computes the sum of the elements of an array, i.e., when the exit is reached, \( s = \sum_{k=0}^{n-1} t[k] \):

\[
\begin{align*}
\text{i, s integer variables} \\
\text{t integer array of length n} \\
\ell_0 : & \ (\text{true}) \\
& \ s = 0; \\
\ell_1 : & \ (s = 0) \\
& \ i = 0; \\
\ell_2 : & \ (i = 0 \land s = 0) \\
& \text{while}(i < n)\{ \\
\ell_3 : & \ (0 \leq i < n \land s = \sum_{k=0}^{i-1} t[k]) \\
& \ s = s + t[i]; \\
\ell_4 : & \ (0 \leq i < n \land s = \sum_{k=0}^{i-1} t[k]) \\
& \ i = i + 1; \\
\ell_5 : & \ (1 \leq i \leq n \land s = \sum_{k=0}^{i-1} t[k]) \\
& \} \\
\ell_6 : & \ (i = n \land s = \sum_{k=0}^{n-1} t[k])
\end{align*}
\]

**Principle of the proof:**

- for each program point \( \ell \), we have a local invariant \( I_\ell \) (denoted by a logical formula instead of a set of states in the figure)
- the global invariant \( I \) is defined by:

\[
I = \{ \langle (\ell_0, m_0), \ldots, (\ell_n, m_n) \rangle \mid \forall n, \ m_n \in I_{\ell_n} \}
\]
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Informal definition: liveness properties

A liveness property is a property which specifies that some (good) behavior will eventually occur.

- **Termination** is a liveness property
  “good behavior”: reaching a blocking state (no more transition available)

- “**State a will eventually be reached by all execution**” is a liveness property
  “good behavior”: reaching state a

- The **absence of runtime errors** is *not* a liveness property

As for safety properties, we intend to provide a **formal definition** of liveness.
Intuition towards a formal definition

How to refute a liveness property?

- We consider liveness property $\mathcal{T}$ (think $\mathcal{T}$ is termination)
- We assume $S$ does not satisfy liveness property $\mathcal{T}$
- Thus, there exists a counter-example trace $\sigma \in \mathcal{J}_S \setminus \mathcal{T}$;
- Let us assume $\sigma$ is actually finite...
  the definition of liveness says some (good) behavior should eventually occur:
  - how do we know that $\sigma$ cannot be extended into a trace $\sigma \cdot \sigma'$ that will satisfy this behavior?
  - maybe that after a few more computation steps, $\sigma$ will reach a blocking state...
Intuition towards a formal definition

To refute a liveness property, we need to look at infinite traces.

Example: if we run a program, and do not see it return...
- should we do Ctrl+C and conclude it does not terminate?
- should we just wait a few more seconds minutes, hours, years?

Towards a formal definition:
- we expect any finite trace be the prefix of a trace in $\mathcal{T}$

... since finite executions cannot be used to disprove $\mathcal{T}$

Formal definition (incomplete)

$$\text{PCI}(\mathcal{T}) = S^*$$
Definition

Formal definition

Operator **Live** is defined by \( \text{Live}(\mathcal{T}) = \mathcal{T} \cup (S^\infty \setminus \text{Safe}(\mathcal{T})) \). Given property \( \mathcal{T} \), the following three statements are equivalent:

\[(i) \quad \text{Live}(\mathcal{T}) = \mathcal{T} \]
\[(ii) \quad \text{PCI}(\mathcal{T}) = S^* \]
\[(iii) \quad \text{Lim} \circ \text{PCI}(\mathcal{T}) = S^\infty \]

When they are satisfied, \( \mathcal{T} \) is said to be a **liveness property**

Example: termination

- The property is \( \mathcal{T} = S^* \)
  (i.e., there should be no infinite execution)
- Clearly, it satisfies \((ii)\): \( \text{PCI}(\mathcal{T}) = S^* \)
  thus termination indeed satisfies this definition
Proof of equivalence

(i) implies (ii):

We assume that $\text{Live}(T) = T$, i.e., $T \cup (S^\infty \setminus \text{Safe}(T)) = T$

therefore, $S^\infty \setminus \text{Safe}(T) \subseteq T$;

let $\sigma \in S^*$, and let us show that $\sigma \in \text{PCI}(T)$; clearly, $\sigma \in S^\infty$, thus:

- either $\sigma \in \text{Safe}(T) = \text{Lim}(\text{PCI}(T))$, so all its prefixes are in $\text{PCI}(T)$
  and $\sigma \in \text{PCI}(T)$
- or $\sigma \in T$, which implies that $\sigma \in \text{PCI}(T)$

(ii) implies (iii):

If $\text{PCI}(T) = S^*$, then $\text{Lim} \circ \text{PCI}(T) = S^\infty$

(iii) implies (i):

If $\text{Lim} \circ \text{PCI}(T) = S^\infty$, then

$\text{Live}(T) = T \cup (S^\infty \setminus (T \cup \text{Lim} \circ \text{PCI}(T))) = T \cup (S^\infty \setminus S^\infty) = T$
We assume that:

- $S = \{a, b, c\}$
- $T$ states that $b$ should eventually be visited, after $a$ has been visited; elements of $T$ can be described by
  
  $$T = S^* \cdot a \cdot S^* \cdot b \cdot S^\infty$$

Then $T$ is a liveness property:

- let $\sigma \in S^*$; then $\sigma \cdot a \cdot b \in T$, so $\sigma \in \text{PCI}(T)$
- thus, $\text{PCI}(T) = S^*$
A property of \textbf{Live}

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\textbf{Theorem}

If $\mathcal{T}$ is a trace property, then $\text{Live}(\mathcal{T})$ is a liveness property (i.e., operator $\text{Live}$ is idempotent).

\textbf{Proof:} we show that $\text{PCI} \circ \text{Live}(\mathcal{T}) = S^*$, by considering $\sigma \in S^*$ and proving that $\sigma \in \text{PCI} \circ \text{Live}(\mathcal{T})$; we first note that:

\[\text{PCI} \circ \text{Live}(\mathcal{T}) = \text{PCI}(\mathcal{T}) \cup \text{PCI}(S^\infty \setminus \text{Safe}(\mathcal{T})) \]
\[= \text{PCI}(\mathcal{T}) \cup \text{PCI}(S^\infty \setminus \text{Lim} \circ \text{PCI}(\mathcal{T}))\]

- if $\sigma \in \text{PCI}(\mathcal{T})$, this is obvious.
- if $\sigma \notin \text{PCI}(\mathcal{T})$, then:
  - $\sigma \notin \text{Lim} \circ \text{PCI}(\mathcal{T})$ by definition of the limit
  - thus, $\sigma \in S^\infty \setminus \text{Lim} \circ \text{PCI}(\mathcal{T})$
  - $\sigma \in \text{PCI}(S^\infty \setminus \text{Lim} \circ \text{PCI}(\mathcal{T}))$ as PCI is extensive, which proves the above result
Outline

1. Safety properties

2. Liveness properties
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3. Decomposition of trace properties

4. A Specification Language: Temporal logic

5. Beyond safety and liveness

6. Conclusion
Termination proof with ranking function

- We consider only termination
- We consider transition system $S = (S, \rightarrow, S_I)$, and liveness property $\mathcal{T}$
- We seek a way of verifying that $S$ satisfies termination, i.e., that $[S]^\infty \subseteq S^*$

**Definition: ranking function**

A **ranking function** is a function $\phi : S \rightarrow E$ where:

- $(E, \sqsubseteq)$ is a well-founded ordering
- $\forall s_0, s_1 \in S, s_0 \rightarrow s_1 \Implies \phi(s_1) \sqsubseteq \phi(s_0)$

**Theorem**

If $S$ has a ranking function $\phi$, it satisfies termination.
Example

We consider the termination of the array sum program:
Proof by variance

- We consider transition system $\mathcal{S} = (S, \rightarrow, S_I)$, and liveness property $\mathcal{T}$; infinite traces semantics is the least fixpoint of $F_{\omega}$.
- We seek a way of verifying that $\mathcal{S}$ satisfies $\mathcal{T}$, i.e., that $[S]^{\infty} \subseteq \mathcal{T}$

Principle of variance proofs

Let $(I_n)_{n \in \mathbb{N}}, I_{\omega}$ be elements of $S^{\infty}$; these are said to form a variance proof of $\mathcal{T}$ if and only if:

- $S^{\infty} \subseteq I_0$
- for all $k \in \{1, 2, \ldots, \omega\}$, $\forall s \in S, \langle s \rangle \in I_k$
- for all $k \in \{1, 2, \ldots, \omega\}$, there exists $l < k$ such that $F_{\omega}(I_l) \subseteq I_k$
- $I_{\omega} \subseteq \mathcal{T}$

Proofs of soundness and completeness: exercise
Outline

1. Safety properties
2. Liveness properties
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The decomposition theorem

Theorem

Let $\mathcal{T} \subseteq S^\infty$; it can be decomposed into the conjunction of safety property $\text{Safe}(\mathcal{T})$ and liveness property $\text{Live}(\mathcal{T})$:

$$\mathcal{T} = \text{Safe}(\mathcal{T}) \cap \text{Live}(\mathcal{T})$$


- Consequence of this result:
  the proof of any trace property can be decomposed into
  - a proof of safety
  - a proof of liveness
Proof

- **Safety part:**
  Safe is idempotent, so $\text{Safe}(\mathcal{T})$ is a safety property.

- **Liveness part:**
  Live is idempotent, so $\text{Live}(\mathcal{T})$ is a liveness property.

- **Decomposition:**

  \[
  \text{Safe}(\mathcal{T}) \cap \text{Live}(\mathcal{T}) = \text{Safe}(\mathcal{T}) \cap (S^\infty \setminus \text{Safe}(\mathcal{T}) \cup \mathcal{T}) \\
  = \text{Safe}(\mathcal{T}) \cap (S^\infty \setminus \text{Safe}(\mathcal{T})) \\
  \cup \text{Safe}(\mathcal{T}) \cap \mathcal{T} \\
  = \emptyset \cup \mathcal{T} \\
  = \mathcal{T}
  \]
Example: verification of total correctness

i, s integer variables
t integer array of length n

\$l_0\ :\ s = 0;\$
\$l_1\ :\ i = 0;\$
\$l_2\ :\ \textbf{while}(i < n)\{$
\$l_3\ :\ s = s + t[i];\$
\$l_4\ :\ i = i + 1;\$
\$l_5\ :\ \}$
\$l_6\ :\ \ldots\$

Property to prove:

total correctness

1. the program terminates
2. and it computes the sum of the elements in the array

Application of the decomposition principle

Conjunction of two proofs:

1. Proved with a \textit{ranking function}
2. Proved with \textit{local invariants}
Safety and Liveness Decomposition Example

We consider a very simple greatest common divider code function:

\[ l_0 : \text{int } f(\text{int } a, \text{int } b)\{ \]
\[ l_1 : \text{while}(a > 0)\{ \]
\[ l_2 : \text{int } d = b / a; \]
\[ l_3 : \text{int } r = b - a * d; \]
\[ l_4 : b = a; \]
\[ l_5 : a = r; \]
\[ l_6 : \} \]
\[ l_7 : \text{return } b; \]
\[ l_8 : \} \]

Specification

When applied to positive integers, function \( f \) should always return their GCD.
Safety and Liveness Decomposition Example

We consider a very simple greatest common divider code function:

```c
int f(int a, int b){
    while(a > 0){
        int d = b/a;
        int r = b - a * d;
        b = a;
        a = r;
    }
    return b;
}
```

**Specification**

When applied to positive integers, function `f` should always return their GCD.

**Safety part**

For all trace starting with positive inputs, a conjunction of two properties:

- no runtime errors
- the value of `b` is the GCD

**Liveness part**

Termination, on all traces starting with positive inputs
The Zoo of semantic properties: current status

Trace properties
- total correctness

Safety properties
- never reach \( s_0 \) before \( s_1 \)

State properties
- absence or runtime errors
- partial correctness

Liveness properties
- termination

- **Safety**: if wrong, can be refuted with a finite trace
  proof done by *invariance*

- **Liveness**: if wrong, has to be refuted with an infinite trace
  proof done by *variance*
Outline

1 Safety properties
2 Liveness properties
3 Decomposition of trace properties
4 A Specification Language: Temporal logic
5 Beyond safety and liveness
6 Conclusion
Notion of specification language

- Ultimately, we would like to **verify or compute** properties
- So far, we simply describe properties with **sets of executions** or worse, with English / French / ... statements
- Ideally, we would prefer to use a **mathematical language** for that
  - to gain in concision, avoid ambiguity
  - to define sets of properties to consider, fix the form of inputs for verification tools...

**Definition: specification language**

A specification language is a set of terms $L$ with an interpretation function (or semantics)

$$[.] : L \rightarrow \mathcal{P}(S^{\infty}) \quad (\text{resp., } \mathcal{P}(S))$$

- We are now going to consider specification languages for states, for traces...
A State specification language

A first example of a (simple) specification language:

A state specification language

- **Syntax:** we let terms of $L_S$ be defined by:

  $$\forall \ell \mid x < x' \mid x < n \mid \neg p \mid p' \land p'' \mid \Omega$$

- **Semantics:** $[\rho] \subseteq S_\Omega$ is defined by

  $$
  
  \begin{align*}
  [\forall \ell] & = \{ \ell \} \times M \\
  [x \leq x'] & = \{(\ell, m) \in S \mid m(x) \leq m(x')\} \\
  [x \leq n] & = \{(\ell, m) \in S \mid m(x) \leq n\} \\
  [\neg p] & = S_\Omega \setminus [\rho] \\
  [p \land p'] & = [p] \cap [p'] \\
  [\Omega] & = \{\Omega\}
  \end{align*}
  $$

**Exercise:** add $\text{add} =, \lor, \implies$...
State properties: examples

Unreachability of control state $l_0$:
- **specification**: $\Omega \lor \neg @l_0$
- **property**: $\left[\Omega \lor \neg @l_0\right] = S_\Omega \setminus \{(l_0, m) \mid m \in M\}$

Absence of runtime errors:
- **specification**: $\neg \Omega$
- **property**: $\left[\neg \Omega\right] = S_\Omega \setminus \{\Omega\} = S$

Intermittent invariant:
- **principle**: attach a local invariant to each control state
- **example**:

$\begin{align*}
l_0 &: \text{if}(x \geq 0)\{ \\
\text{if } x \geq 0 & \{ y = x; \} \quad @l_1 \implies x \geq 0 \\
\text{if } x < 0 & \{ y = -x; \} \quad @l_2 \implies x < 0 \\
\text{else } & \{ y = -x; \} \quad @l_3 \implies y \geq 0 \\
\text{if } x < 0 & \{ y = -x; \} \quad @l_4 \implies x < 0 \land y > 0 \\
\text{else } & \{ y = x; \} \quad @l_5 \implies y \geq 0
\end{align*}$
Propositional temporal logic: syntax

We now consider the **specification of trace properties**

- **Temporal logic**: specification of properties in terms of events that occur at distinct times in the execution (hence, the name “temporal”)
- There are **many** instances of temporal logic
- We study a simple one: **Pnueli’s Propositional Temporal Logic**

**Definition: syntax of PTL (Propositional Temporal Logic)**

Properties over traces are defined as terms of the form

\[
  t \in \mathcal{L}_{\text{PTL}} \quad ::= \
  \begin{array}{ll}
    p & \text{state property, i.e., } p \in \mathcal{L}_S \\
    t' \lor t'' & \text{disjunction} \\
    \neg t' & \text{negation} \\
    \bigcirc t' & \text{"next"} \\
    t' \mathcal{U} t'' & \text{"until"}, \text{i.e., } t' \text{ until } t''
  \end{array}
\]
Some operators on traces:

- $|\sigma|$ denotes the **length** of trace $\sigma$ (either an integer or $\infty$)
- "tail" operator $\cdot_{i\rceil}$:

  $$
  \sigma_{i\rceil} = \varepsilon \text{ if } |\sigma| < i
  $$

  $$
  (s_0, \ldots, s_i) \cdot \sigma_{i-1\rceil} := \sigma \text{ otherwise}
  $$

Semantics of Propositional Temporal Logic formulae

- $[p] = \{ s \cdot \sigma \mid s \in [p] \wedge \sigma \in S^\infty \}$
- $[t_0 \lor t_1] = [t_0] \cup [t_1]$
- $[-t_0] = S^\infty \setminus [t_0]$
- $[\bigcirc t_0] = \{ s \cdot \sigma \mid s \in S \wedge \sigma \in [t_0] \}$
- $[t_0 \bigcup t_1] = \{ \sigma \in S^\infty \mid \exists n \in \mathbb{N}, \forall i < n, \sigma_{i\rceil} \in [t_0] \wedge \sigma_{n\rceil} \in [t_1] \}$
Temporal logic operators as syntactic sugar

Many useful operators can be added:

- **Boolean constants:**
  
  \[
  \text{true} ::= (x < 0) \lor \neg(x < 0) \\
  \text{false} ::= \neg\text{true}
  \]

- **Sometime:**
  
  \[
  \diamond t ::= \text{true} \land t
  \]

  **intuition:** there exists a rank \( n \) at which \( t \) holds

- **Always:**
  
  \[
  \Box t ::= \neg(\diamond(\neg t))
  \]

  **intuition:** there is no rank at which the negation of \( t \) holds

**Exercise:** what do \( \diamond \Box t \) and \( \Box \diamond t \) mean?
Propositional temporal logic: examples

We consider the program below:

\[
\begin{align*}
\ell_0 & : \textbf{int } x = \text{input}(); \\
\ell_1 & : \textbf{if}(x < 8) \{ \\
\ell_2 & : \quad x = 0; \\
\ell_3 & : \} \textbf{else } \{ \\
\ell_4 & : \quad x = 1; \\
\ell_5 & : \} \\
\ell_6 & : \ldots 
\end{align*}
\]

Examples of properties:

- “when \( \ell_4 \) is reached, \( x \) is positive”
  \[
  \Box((\neg \ell_4 \implies x \geq 0)
  \]

- “if the value read at point \( \ell_0 \) is negative, and when \( \ell_6 \) is reached, \( x \) is equal to 0”
  \[
  (\neg \ell_1 \land x < 0) \implies \Box((\neg \ell_6 \implies x = 0)
  \]
Outline

1. Safety properties
2. Liveness properties
3. Decomposition of trace properties
4. A Specification Language: Temporal logic
5. Beyond safety and liveness
6. Conclusion
Beyond safety and liveness

Security properties

We now consider other interesting properties of programs, and show that they do not all reduce to trace properties.

Security

- Collects many kinds of properties
- So we consider just one:

  an unauthorized observer should not be able to guess anything about private information by looking at public information

- **Example:** another user should not be able to guess the content of an email sent to you
- We need to **formalize this property**
A few definitions

Assumptions:

- We let $S = (S, \rightarrow, S_I)$ be a transition system.
- States are of the form $(\ell, m) \in L \times M$.
- Memory states are of the form $X \rightarrow \mathcal{V}$.
- We let $\ell, \ell' \in L$ (program entry and exit).
- and $x, x' \in X$ (private and public variables).

Security property we are looking at

Observing the value of $x'$ at $\ell'$ gives no information on the value of $x$ at $\ell$.

We consider the transformer $\Phi$ defined by:

$$\Phi : M \longrightarrow \mathcal{P}(M)$$

$$m \longmapsto \{ m' \in M \mid \exists \sigma = \langle (\ell, m), \ldots, (\ell', m') \rangle \in \llbracket S \rrbracket \}$$
Non-interference

Definition: non-interference

There is no interference between \((\ell, x)\) and \((\ell', x')\) and we write \((\ell', x') \not\to (\ell, x)\) if and only if the following property holds:

\[
\forall m \in M, \forall v_0, v_1 \in V, \\
\{ m'(x') | m' \in \Phi(m[x \leftarrow v_0]) \} = \{ m'(x') | m' \in \Phi(m[x \leftarrow v_1]) \}
\]

Intuition:

- if two observations at point \(\ell\) differ only in the value of \(x\), there is no difference in observation of \(x'\) at \(\ell'\)
- in other words, observing \(x'\) at \(\ell'\) (even on many executions) gives no information about the value of \(x\) at point \(\ell\)...
Non-interference is not a trace property

- We assume $V = \{0, 1\}$ and $X = \{x, x'\}$ (store $m$ is defined by the pair $(m(x), m(x'))$, and denoted by it).
- We assume $L = \{l, l'\}$ and consider two systems such that all transitions are of the form $(l, m) \to (l', m')$ (i.e., system $S$ is isomorphic to its transformer $\Phi[S]$)

$$
\Phi[S_0] : 
\begin{align*}
(0, 0) & \mapsto M \\
(0, 1) & \mapsto M \\
(1, 0) & \mapsto M \\
(1, 1) & \mapsto M \\
\end{align*}
$$

$$
\Phi[S_1] : 
\begin{align*}
(0, 0) & \mapsto M \\
(0, 1) & \mapsto M \\
(1, 0) & \mapsto \{(1, 1)\} \\
(1, 1) & \mapsto \{(1, 1)\} \\
\end{align*}
$$

- $S_1$ has fewer behaviors than $S_0$: $[S_1]^* \subset [S_0]^*$
- $S_0$ has the non-interference property, but $S_1$ does not
- If non interference was a trace property, $S_1$ should have it (monotony)

Thus, the non interference property is not a trace property
Dependence properties

### Dependence property

- Many notions of dependences
- So we consider just one:

  **what inputs may have an impact on the observation of a given output**

**Applications:**
- **reverse engineering:** understand how an input gets computed
- **slicing:** extract the fragment of a program that is relevant to a result

- This corresponds to the **negation** of non-interference
### Definition: interference

There is **interference** between \((\ell, x)\) and \((\ell', x')\) and we write \((\ell', x') \rightsquigarrow (\ell, x)\) if and only if the following property holds:

\[
\exists m \in M, \exists v_0, v_1 \in V,
\{ m'(x') \mid m' \in \Phi(m[x \leftarrow v_0]) \} \neq \{ m'(x') \mid m' \in \Phi(m[x \leftarrow v_1]) \}
\]

- This expresses that there is at least one case, where the value of \(x\) at \(\ell\) has an impact on that of \(x'\) at \(\ell'\).
- It may not hold even if the computation of \(x'\) reads \(x\):

\[
\ell : \quad x' = 0 \star x; \\
\ell' : \quad \ldots
\]
Beyond safety and liveness

Interference is not a trace property

- We assume $V = \{0, 1\}$ and $X = \{x, x'\}$ (store $m$ is defined by the pair $(m(x), m(x'))$, and denoted by it)
- We assume $L = \{\ell, \ell'\}$ and consider two systems such that all transitions are of the form $(\ell, m) \rightarrow (\ell', m')$
  (i.e., system $S$ is isomorphic to its transformer $\Phi[S]$)

\[
\begin{align*}
\Phi[S_0] : & \quad (0, 0) \quad \mapsto \quad M \\
& \quad (0, 1) \quad \mapsto \quad M \\
& \quad (1, 0) \quad \mapsto \quad \{(1, 1)\} \\
& \quad (1, 1) \quad \mapsto \quad \{(1, 1)\}
\end{align*}
\]

\[
\begin{align*}
\Phi[S_1] : & \quad (0, 0) \quad \mapsto \quad \{(1, 1)\} \\
& \quad (0, 1) \quad \mapsto \quad \{(1, 1)\} \\
& \quad (1, 0) \quad \mapsto \quad \{(1, 1)\} \\
& \quad (1, 1) \quad \mapsto \quad \{(1, 1)\}
\end{align*}
\]

- $S_1$ has fewer behavior than $S_0$: $[S_1]^* \subset [S_0]^*$
- $S_0$ has the interference property, but $S_1$ does not
- If interference was a trace property, $S_1$ should have it (monotony)

Thus, the interference property is not a trace property
Outline

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The Zoo of semantic properties

Sets of sets of executions
non-interference, dependency

Trace properties
total correctness

Safety properties
never reach $s_0$ before $s_1$

State properties
absence or runtime errors
partial correctness

Liveness properties
termination
Summary

To sum-up:

- **Trace properties** allow to express a large range of program properties
- **Safety** = absence of bad behaviors
- **Liveness** = existence of good behaviors
- Trace properties can be decomposed as conjunctions of safety and liveness properties, with dedicated proof methods
- Some interesting properties are not trace properties
  - security properties are sets of sets of executions
- Notion of **specification languages** to describe program properties