

3rd Millennium Computing

1. Binary Values

Number, Text, Sound, Image, Measure, Command, Packet ...

2. Discrete Time

- Asynchronous: Neurons, Internet, Captors...
- Synchronous: Circuits, Airplanes, GPS...

3. Time Scales

- $1 \text{ s} = 10^3 \text{ ms} = 10^6 \text{ }\mu\text{s} = 10^9 \text{ ns} = 10^{12} \text{ ps} = 10^{15} \text{ fs} = 10^{18} \text{ as}$
- Heart -> Nerve -> Neuron -> μ Proc -> Circuit -> Molecule->Photon
- 1 *attosecond* \approx time for light to travel 3 hydrogen atoms
- 100 *attoseconds* \approx shortest time measured as of 2004 \approx 16 decimal digits

Log₁₀ scale

s/hour	s/day	s/week	s/year	ns/s	ns/day	ns/month	ns/year	ns/century
4	5	6	7	9	14	15	16	18

Synchronous Circuits

1. Digital Algebra

- Infinite synchronous binary sequences
- Hacking Science

2. Boolean Normal Forms

- Decision Diagrams
- Integer Operations

3. Regular Normal Forms

Cookie: Moore's Law

- Regular dimension
- Minimal NDXA

4. Circuit Compiler

- From C to NetList
- From NetList to ASIC

Binary Integers

n	B_0^n	B_1^n	B_2^n	B_3^n	$B_{4...}^n$	$[n]$	$\{k \in n\}$
0	0	0	0	0	0^{00}	0^{00}	$\{\}$
1	1	0	0	0	0^{00}	10^{00}	$\{0\}$
2	0	1	0	0	0^{00}	010^{00}	$\{1\}$
3	1	1	0	0	0^{00}	110^{00}	$\{0,1\}$
4	0	0	1	0	0^{00}	0010^{00}	$\{2\}$
5	1	0	1	0	0^{00}	1010^{00}	$\{0,2\}$
6	0	1	1	0	0^{00}	0110^{00}	$\{1,2\}$
7	1	1	1	0	0^{00}	1110^{00}	$\{0,1,2\}$
8	0	0	0	1	0^{00}	00010^{00}	$\{3\}$

n	B_0^n	B_1^n	B_2^n	B_3^n	$B_{4...}^n$
-1	1	1	1	1	1^{00}
-2	0	1	1	1	1^{00}
-3	1	0	1	1	1^{00}
-4	0	0	1	1	1^{00}
-5	1	1	0	1	1^{00}
-6	0	1	0	1	1^{00}
-7	1	0	0	1	1^{00}
-8	0	0	0	1	1^{00}
-9	1	1	1	0	1^{00}

$$k \in n \Leftrightarrow 1 = B_k^n$$

$$n = \sum_k B_k^n 2^k = \sum_{k \in n} 2^k$$

$$n \in \mathbb{N} \Leftrightarrow v(n) = \sum_{k \in \mathbb{N}} B_k^n = |\{k \in n\}| < \infty$$

$$-0 = 1 + 2 + 4 + 8 + 16 + \dots = -1$$

$$\neg n = -(n + 1)$$

$$n \in \mathbb{Z} \Leftrightarrow \exists l, \forall k : k > l \Rightarrow B_k^n = B_l^n \Leftrightarrow \text{ultimately constant.}$$

Magic Masks

$$n = \sum_k B_k^n 2^k = \sum_{k \in n} 2^k$$

$$\begin{aligned} \mu^0 & : 10101010\dots \\ +2\mu^0 & : 01010101\dots \\ = 3\mu^0 & : 11111111\dots \\ 3\mu^0 & = -1 \end{aligned}$$

$$\mu^k = \sum_n B_k^{-n} 2^n = \sum_{n \notin k} 2^n$$

B_0^n	B_1^n	B_2^n
1	1	1
0	1	1
1	0	1
0	0	1
1	1	0
-1/3	-1/5	-1/17

$$\mu_0 = 1 + 4 + 16 + \dots = -\frac{1}{3}$$

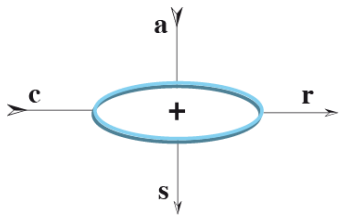
$$\mu^k = (1^{2^k} 0^{2^k})^\infty$$

$$\mu^k = \frac{-1}{1 + 2^{2^k}}$$

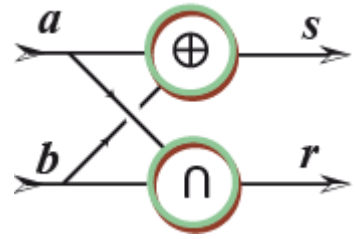
Synchronous Gates

VDD

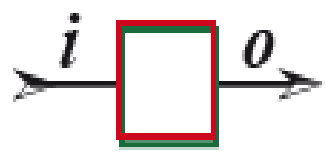
$$-1 = 1^\omega = [N \rightarrow 1]$$



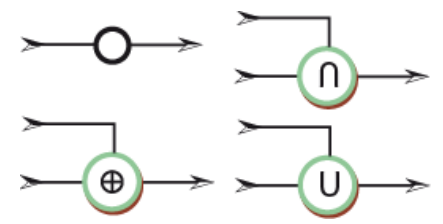
HalfAdder



Register



Boolean Gates



$$s_t = a_t \oplus b_t = a_t + b_t - 2a_t b_t$$

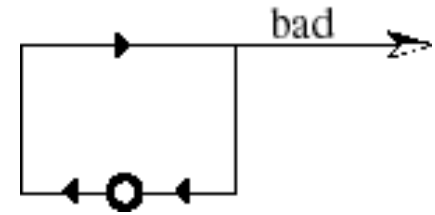
$$r_t = a_t \cap b_t = a_t b_t$$

$$o_0 = 0, o_{t+1} = i_t$$

Feedback

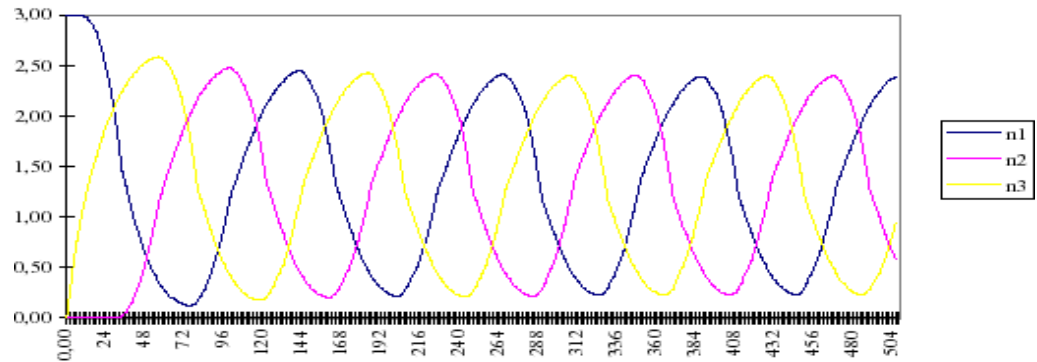
Problem:

$$\text{bad} = \neg \text{bad}$$



$$\text{bad}_0 = 1 - \text{bad}_0$$

$$\text{bad}_0 = \frac{1}{2} \notin \{0,1\}$$



No problem:

$$\text{tog} = \text{REG}(\neg \text{tog})$$

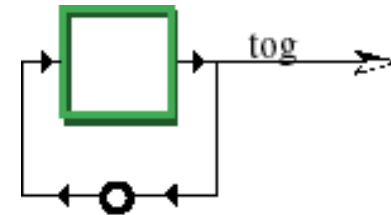
$$\text{tog}_0 = 0$$

$$\text{tog}_{2N} = 0$$

$$\text{tog}_{N+1} = 1 - \text{tog}_N$$

$$\text{tog}_{2N+1} = 1$$

$$\neg \text{tog} = \mu^0 = (10)^\infty = -1/3$$

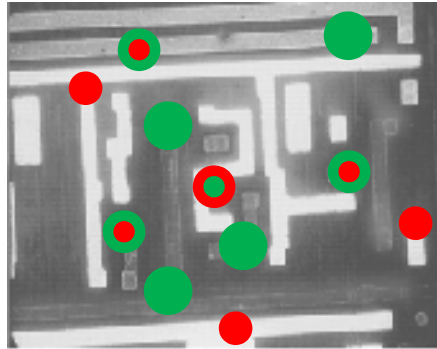


Solution: No combinational feedback !

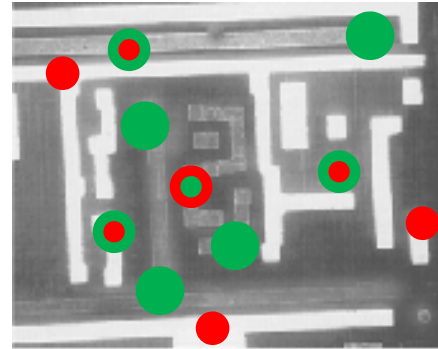
Pulse Code modulation

EBM
Images:
1GHz
Circuit

Time=0 or even...







Time=1 or odd...

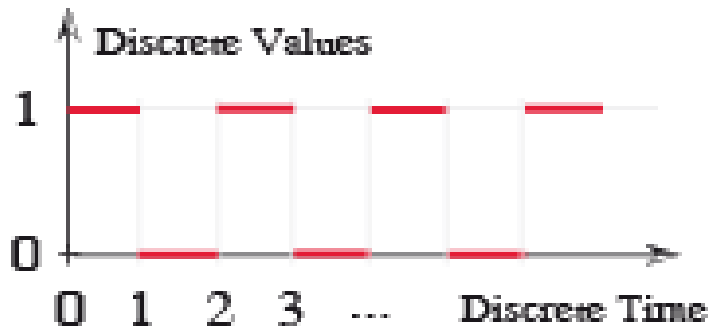


10 μm

=> 4 Binary
Transitions

$0 \rightarrow 0$  $pcm(t) = 0$
 $0 \rightarrow 1$  $t < 1 \Rightarrow pcm(t) = 0$
 $1 = pcm(t) + pcm(t+1)$

$1 \rightarrow 1$  $pcm(t) = 1$
 $1 \rightarrow 0$  $t < 1 \Rightarrow pcm(t) = 1$
 $1 = pcm(t) + pcm(t+1)$
 $pcm(t) = \lfloor t \rfloor \cap 1$



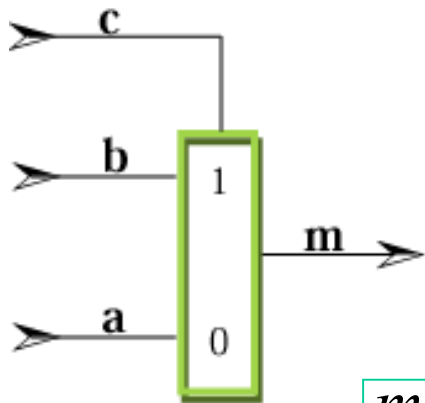
$$dirac(s)(t) = \sum_{n \in \mathbb{N}} \delta(n) s_n$$

$$pcm(s)(t) = \int_{\lfloor t \rfloor}^{\lfloor t \rfloor + 1} dirac(s)(x) dx$$

Digital Synchronous Circuit

- Syntax
 - Compose *<Vdd, And, Xor, Reg>*
 - Feed-forward Boolean Gates
 - Hierarchical Synthesis
- Semantics
 - Binary Values
 - Zero delay: *<In, Not, And, Xor, Reg>*
 - All variables have binary values at all times!
 - Discrete Time
 - All *Registers* have the very same unit delay.
 - Constant or variable time period.

Multiplexer



$$m = \text{mux}(c, b, a)$$

$$c_N = 0 \Rightarrow m_N = a_N$$

$$c_N = 1 \Rightarrow m_N = b_N$$

$$m = (c \cap b) \cup (\neg c \cap a)$$

$$m_N = c_N b_N + (1 - c_N) a_N$$

$$m = a \oplus (c \cap (a \oplus b))$$

$$\text{mux} \in \langle \neg, \cap, \cup \rangle$$

$$\text{mux} \in \langle \cap, \oplus \rangle$$

$$\neg x = \text{mux}(x, 0, \neg 0)$$

$$x \cap y = \text{mux}(x, y, x)$$

$$x \cup y = \text{mux}(x, x, y)$$

$$\neg, \cap, \cup \in \langle 0, \neg 0, \text{mux} \rangle$$

$$f \in \langle 0, \bar{0}, x_1, \dots, x_i, \text{mux} \rangle$$

Shannon Decomposition

$$\begin{aligned} f(x_1 x_2 \cdots x_i) &= (\neg x_1 \cap f(0x_2 \cdots x_i)) \cup (x_1 \cap f(1x_2 \cdots x_i)) \\ &= \text{mux}(x_1, f(1x_2 \cdots x_i), f(0x_2 \cdots x_i)) \\ &= (1 - x_1) f(0x_2 \cdots x_i) + x_1 f(1x_2 \cdots x_i) \end{aligned}$$

Boolean Bases

$\langle op_1, \dots, op_k \rangle$ is a Boolean Base =:

$$\forall f \in \mathbb{B}^n \rightarrow \mathbb{B} :$$

$$f(x_1, \dots, x_n) \in \langle x_1, \dots, x_n, op_1, \dots, op_k \rangle$$

$$\langle 0, \neg 0, \text{mux} \rangle$$

$$\langle 0, \neg, \cap, \cup, \oplus \rangle$$

$$\langle \neg, \cap, \cup \rangle$$

$$0 = x \oplus x$$

$$a \oplus b = (a \cap \neg b) \cup (\neg a \cap b)$$

$$\langle x, \neg \text{mux} \rangle$$

$$-1 \notin \langle \oplus, \cap, \cup \rangle = \langle \oplus, \cap \rangle$$

$$-1 = \neg 0 = {}_2 1^\infty = \frac{1}{1-z} = \sum_{n \in \mathbb{N}} 2^n$$

$$\langle -1, \cap, \oplus \rangle = \langle -1, \text{halfAdd} \rangle$$

$$\neg x = \neg 0 \oplus x$$

$$a \cup b = a \oplus b \oplus (a \cap b)$$

Technology Bases

$$\neg a = a \overline{\cap} a$$
$$a \cup b = (\neg a) \overline{\cap} (\neg b)$$
$$a \cap b = \neg(a \overline{\cap} b)$$

Nand $a \overline{\cap} b = \neg(a \cap b)$

Universal Gate $\forall f : f(x_1, \dots, x_n) \in \langle x_1, \dots, x_n, \overline{\cap} \rangle$

Wolfram's Axiom $c = ((a \overline{\cap} b) \overline{\cap} c) \overline{\cap} (a \overline{\cap} ((a \overline{\cap} c) \overline{\cap} a))$

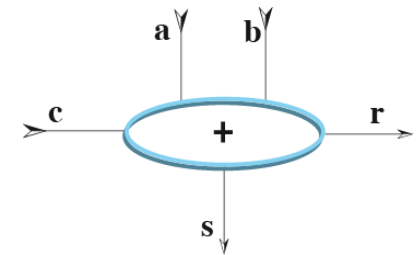
Nor $a \overline{\cup} b = \neg(a \cup b)$

ASIC Gates

Feedback Transistor networks: 4 states

Full Adder

Icon



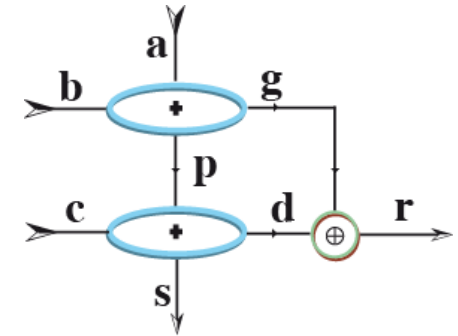
$$\text{fullAdd}(a,b,c) = (s,r)$$

$$\{ (p,g) = \text{halfAdd}(a,b)$$

$$(s,d) = \text{halfAdd}(c,g)$$

$$r = g \oplus d \}$$

Definition

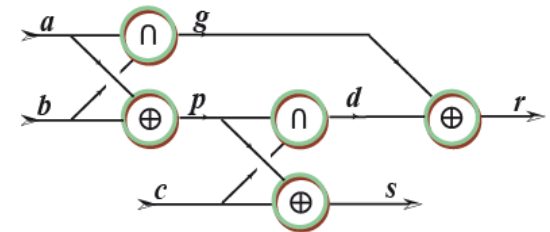


$$p = a \oplus b \quad g = a \cap b$$

$$s = c \oplus p \quad d = c \cap p$$

$$r = g \oplus d$$

Netlist



$$\begin{aligned} \forall t: S_t &= a_t + b_t + c_t = s_t + 2r_t \\ S_t \cdot 2 &= s_t = a_t \oplus b_t \oplus c_t \\ S_t \div 2 &= r_t = a_t b_t \oplus b_t c_t \oplus c_t a_t \end{aligned}$$

Invariant

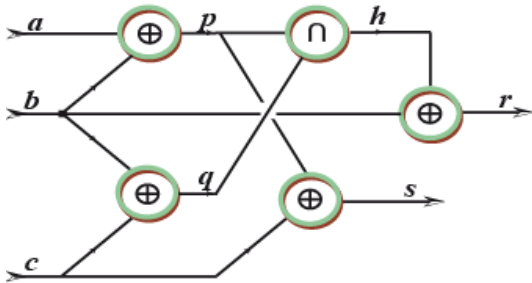
$$a + b + c = s + 2r$$

$$a + b = p + 2g$$

$$c + p = s + 2d$$

$$r = g + d$$

Circuit Simulations



$$p = a \oplus b \quad q = b \oplus c$$

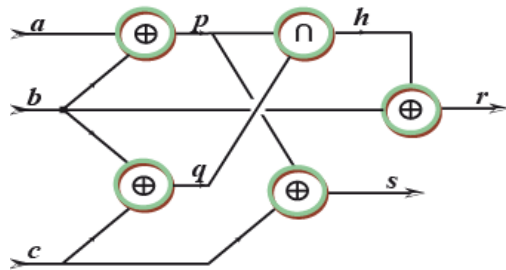
$$s = c \oplus p \quad h = p \cap q$$

$$r = b \oplus h$$

a	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0
b	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1
c	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
p	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	1	1	1
q	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
s	0	0	0	1	1	1	1	0	0	0	0	0	1	1	1	1	1	0	0
h	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
r	0	0	0	0	0	0	0	0	1	1	1	0	0	1	1	1	1	1	1

- Stable Gates + Stable Composition yield Stable Circuit
- Delay Invariant Fixpoint – Binary Values
- Sufficient: Synchronous Clock Delay > Critical Path
- *Gate Level Simulators: logical & electrical*
- *Event Driven Simulators: logical & electrical*

Computational Circuit Proofs

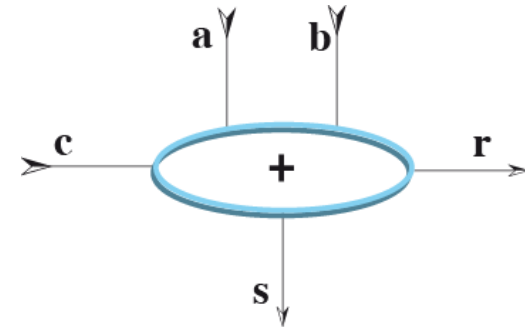


$$p = a \oplus b \quad q = b \oplus c$$

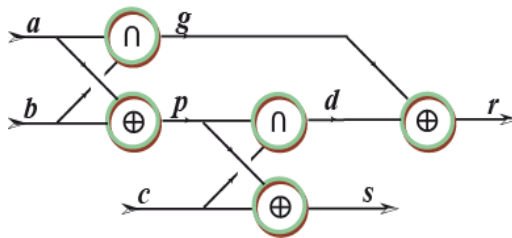
$$s = c \oplus p \quad h = p \cap q$$

$$r = b \oplus h$$

a	0	1	0	1	0	1	0	1
b	0	0	1	1	0	0	1	1
c	0	0	0	0	1	1	1	1
p	0	1	1	0	0	1	1	0
q	0	0	1	1	1	1	0	0
s	0	1	1	0	1	0	0	1
h	0	0	1	0	0	1	0	0
r	0	0	0	1	0	1	1	1
V	0	0	0	0	0	0	0	0



$$a + b + c = s + 2r$$



$$p = a \oplus b \quad g = a \cap b$$

$$s = c \oplus p \quad d = c \cap p$$

$$r = g \oplus d$$

a	0	1	0	1	0	1	0	1
b	0	0	1	1	0	0	1	1
c	0	0	0	0	1	1	1	1
p	0	1	1	0	0	1	1	0
g	0	0	0	1	0	0	0	1
s	0	1	1	0	1	0	0	1
d	0	0	0	0	0	1	1	0
r	0	0	0	1	0	1	1	1
V	0	0	0	0	0	0	0	0

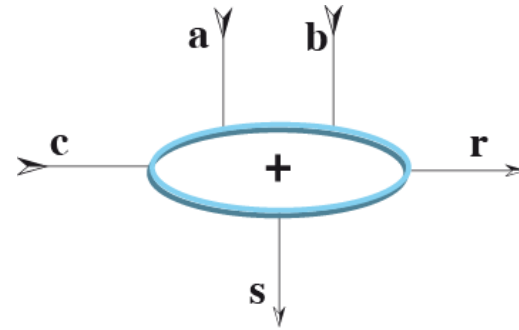
1. Topological Sort
2. Use word length 2^i computer
3. Assign magic masks to inputs
4. Evaluate gates in order - i.e.
5. Compute $g2^i$ Boolean results
6. Extract output truth-tables
7. Verify all invariants

Symbolic Circuit Proof

$$s = a \oplus b \oplus c$$

$$r = a \cap b \oplus b \cap c \oplus c \cap a$$

Specification



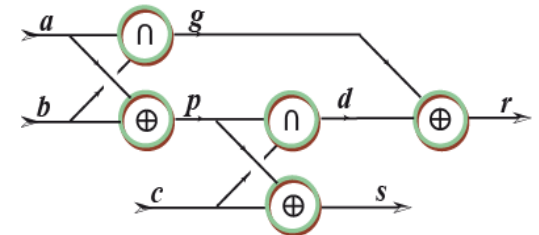
$$s = (a \oplus b) \oplus c$$

$$r = a \cap b \oplus (a \oplus b) \cap c$$

$$p = a \oplus b \quad g = a \cap b$$

$$s = c \oplus p \quad d = c \cap p$$

$$r = g \oplus d$$



Reduce by sharing all equal variables

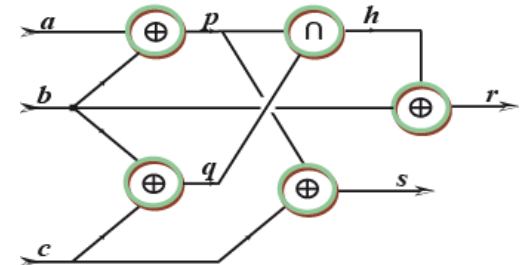
$$s = (a \oplus b) \oplus c$$

$$r = b \oplus (a \oplus b) \cap (b \oplus c)$$

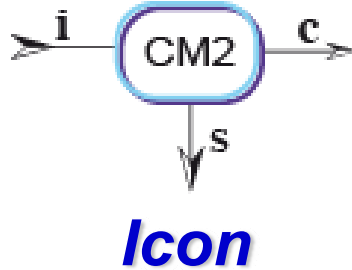
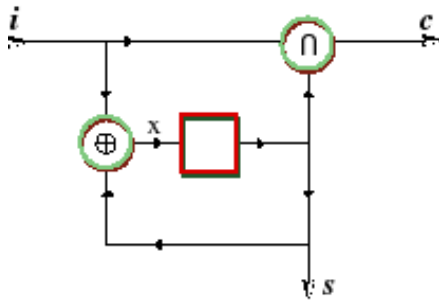
$$p = a \oplus b \quad q = b \oplus c$$

$$s = c \oplus p \quad h = p \cap q$$

$$r = b \oplus h$$



Counter modulo 2



$$cm2(i) = (s, c)$$

$$\{s = z(i \oplus s) \quad c = i \cap s\}$$

Specification

s	0	1	0	1
i	0	0	1	1
x	0	1	1	0
c	0	0	0	1

$$s_0 = 0 \quad s_{t+1} = x_t$$

$$x_t = i_t \oplus s_t$$

$$c_t = i_t s_t$$

Behaviour

$$s = zx$$

$$x = i \oplus s \quad \text{Netlist}$$

$$c = i \cap s$$

$$x(z) = i(z) + zx(z) = \frac{i(z)}{1-z} \pmod{2}$$

$$x_t = \sum_{k \leq t} i_k \pmod{2}$$

$$\forall t \in \mathbb{N}: \sum_{k < t} i_k = s_t + 2 \sum_{k < t} c_k$$

Invariants

State Transitions Truth Table

Counter modulo 4



Icon



Specification

$$cm4(i) = (s[0,1], c)$$

$$\{(s[0], c[1]) = cm2(i, s[0]) \quad (s[1], c) = cm2(c[1], s[1])\}$$

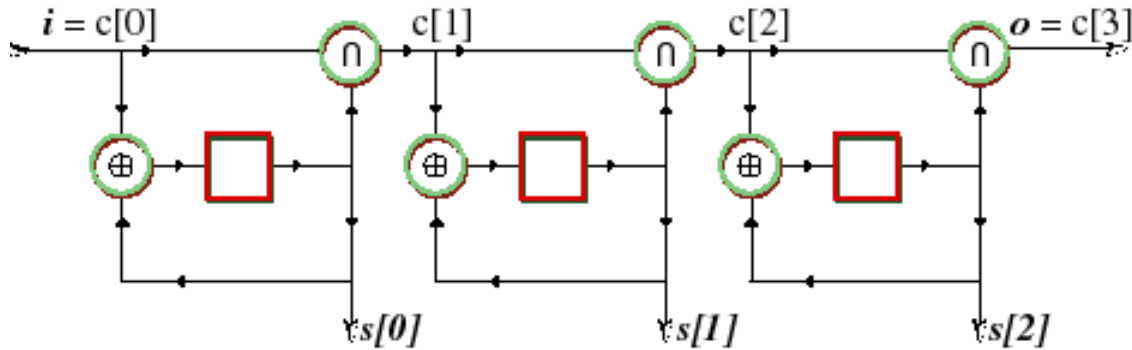
Netlist

$c[0] = i$	$s[0] = zx[0]$	$s[1] = zx[1]$
	$x[0] = c[0] \oplus s[0]$	$x[1] = c[1] \oplus s[1]$
$c = c[2]$	$c[1] = x[0] \cap s[0]$	$c[2] = x[1] \cap s[1]$

Invariant

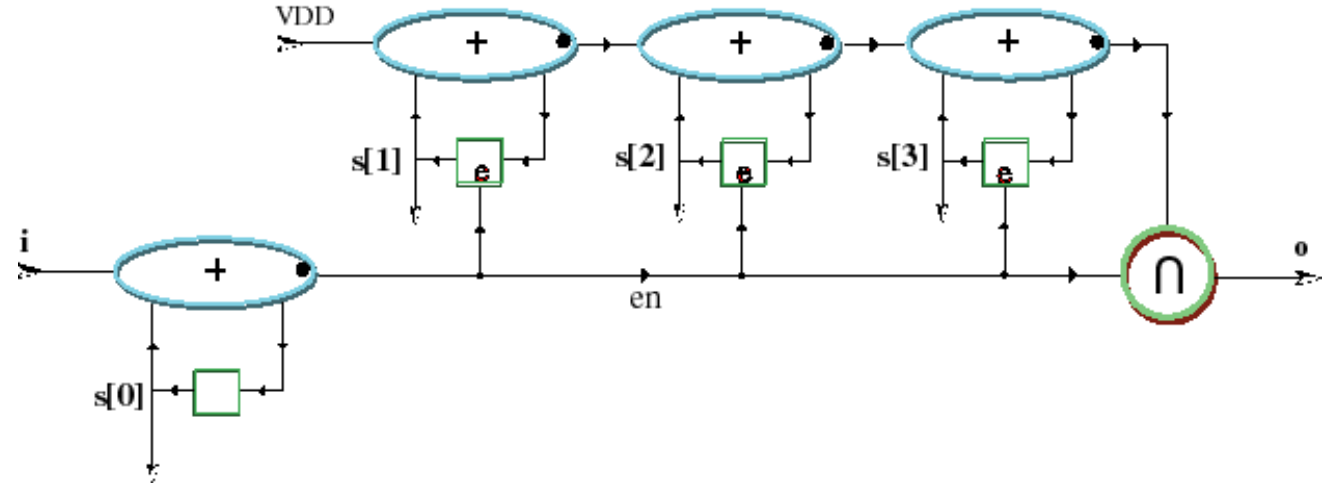
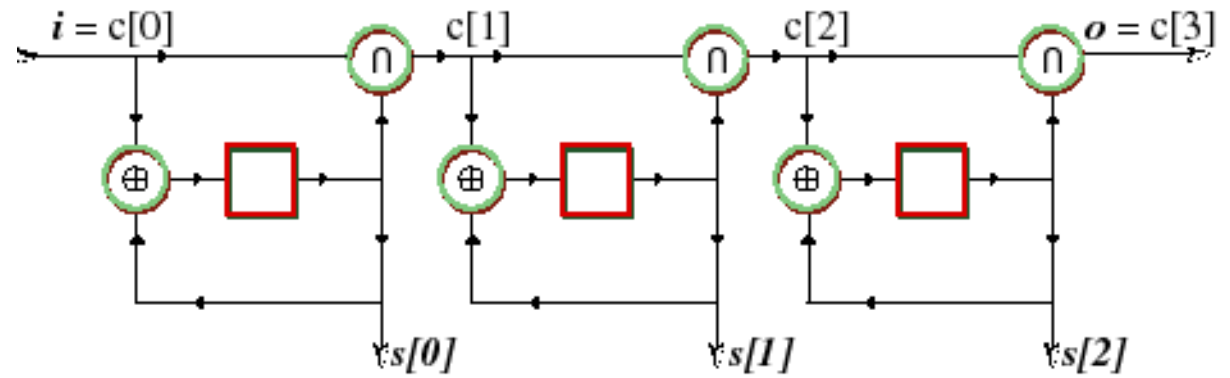
$$\forall t \in \mathbb{N}: \sum_{k < t} i_k = s_t[0] + 2s_t[1] + 4 \sum_{k < t} c_k$$

Binary Counter



cycle	$i = c[0]$	$s[0]$	$s[1]$	$s[2]$	$c[1]$	$c[2]$	$c[3]$
0	1	0	0	0	0	0	0
1	1	1	0	0	1	0	0
2	1	0	1	0	0	0	0
3	1	1	1	0	1	1	0
4	1	0	0	1	0	0	0
5	1	1	0	1	1	0	0
6	1	0	1	1	0	0	0
7	1	1	1	1	1	1	1
8	1	0	0	0	0	0	0

Fast Silicon Counter



Memory-less Circuits

- Boolean Gates
- No feedback
- Topological order
- Reduced
- Automatic Verification
- Automatic Synthesis
- Automatic Optimization
- All symbolic computations

Boolean Normal Forms:

BDD $\langle 0, \neg 0, mux \rangle$

$$mux(x, a, b) = x \cap a \oplus \neg x \cap a$$

where x is an input variable.

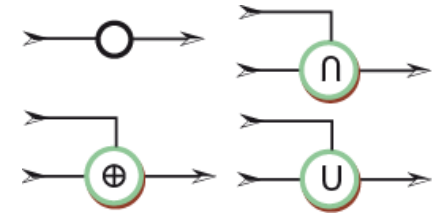
Input order x_1, \dots, x_n

BMD $\langle \neg 0, lin \rangle$

$$lin(a, x, c) = a \oplus x \cap c$$

where x is an input variable.

Logic Gates



$$\neg a = [t \rightarrow (1 - B_t^a)]$$

$$a \cap b = [t \rightarrow (B_t^a \times B_t^b)]$$

$$a \cup b = [t \rightarrow (B_t^a + B_t^b - B_t^a \times B_t^b)]$$

$$a \oplus b = [t \rightarrow (B_t^a + B_t^b - 2B_t^a \times B_t^b)]$$

Arithmetic Definition

$$\neg a = \{t \notin a\}$$

$$a \cap b = \{t \in a\} \cap \{t \in b\}$$

$$a \cup b = \{t \in a\} \cup \{t \in b\}$$

$$a \oplus b = \{t \notin a \cap b\} \cap (\{t \in a\} \cup \{t \in b\})$$

Set Definition

n	B_0^n	B_1^n	B_2^n	B_3^n	$B_{4...}^n$	$w[n]$	$\{k \in n\}$
0	0	0	0	0	0^ω	0^ω	$\{\}$
1	1	0	0	0	0^ω	ε	$\{0\}$
2	0	1	0	0	0^ω	0	$\{1\}$
3	1	1	0	0	0^ω	1	$\{0,1\}$
4	0	0	1	0	0^ω	00	$\{2\}$
5	1	0	1	0	0^ω	10	$\{0,2\}$
6	0	1	1	0	0^ω	01	$\{1,2\}$
7	1	1	1	0	0^ω	11	$\{0,1,2\}$
8	0	0	0	1	0^ω	000	$\{3\}$

$$n \in \mathbb{D}: \\ n = [t \rightarrow B_t^n] \\ \{t \in n\} = \{t \in \mathbb{N} : 1 = B_t^n\}$$

binary sequence $\mathbb{N} \rightarrow \mathbb{B}$
integer subset $2^{\mathbb{N}}$

$$k \in a \Leftrightarrow B_k^a = 1$$

$$\min\{n : 5 \in n\} = ?$$

$$a \subseteq b \Leftrightarrow \{k \in a\} \subseteq \{k \in b\} \Leftrightarrow a = a \cap b$$

$$\forall a, b \in \mathbb{N} \quad (a \subset b) \Rightarrow (a < b) \quad \text{subset order}$$

Can we replace \mathbb{N} by \mathbb{Z} , \mathbb{P} ?

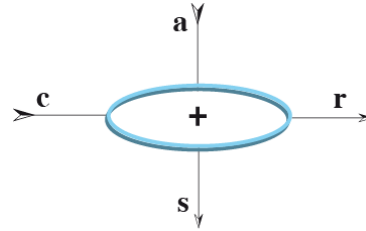
$$\min\{n : k \in n\} = 2^k$$

Binary Field

$$\mathbb{F}_2 = \langle 0 \quad 1 \quad \oplus \quad \cap \rangle$$

\oplus	0	1
0	0	1
1	1	0

\cap	0	1
0	0	0
1	0	1



$$a \cap b = a \times b$$

$$a \oplus b = a + b - 2ab$$

$$\neg a = -0 \oplus a$$

$$a \cup b = a \oplus b \oplus a \cap b$$

	\neg
0	1
1	0

\cup	0	1
0	0	1
1	1	1

\mathbb{F}_2 is a Boolean Ring.
 $0 = a \oplus a$
 $a = a \cap a$

\mathbb{F}_2 is the one and only Boolean Ring which is a Field: $1=1/1$.

A size 2 Boolean Algebra is isomorphic to $\mathbb{F}_2 = \langle 0 \quad 1 \quad \neg \quad \cap \quad \cup \rangle$.

Boolean Algebra

$$n \in \mathbb{D}: \\ n = [t \rightarrow B_t^n] \\ \{t \in n\} = \{t \in \mathbb{N}: 1 = B_t^n\}$$

$$\text{binary sequence } \mathbb{N} \rightarrow \mathbb{B} \\ \text{integer subset } 2^{\mathbb{N}}$$

Boolean Algebra : $\langle \mathbb{D} \quad \neg \quad \cap \quad \cup \rangle$

De Morgan

$$\neg(a \cup b) = \neg a \cap \neg b \\ \neg(a \cap b) = \neg a \cup \neg b$$

$$a \cap b = b \cap a \\ a \cap (b \cap c) = (a \cap b) \cap c \\ a \cap \neg 0 = a \\ a \cap \neg a = 0 \\ a \cap (b \cup c) = (a \cap b) \cup (a \cap c)$$

$$a \cup b = b \cup a \\ a \cup (b \cup c) = (a \cup b) \cup c \\ a \cup 0 = a \\ a \cup \neg a = \neg 0 \\ a \cup (b \cap c) = (a \cup b) \cap (a \cup c)$$

Boolean Ring: $\langle \mathbb{D} \quad \oplus \quad \cap \rangle$

$$a \oplus b = (\neg a \cap b) \cup (a \cap \neg b) \\ a \cup b = a \oplus b \oplus (a \cap b) \\ \neg a = a \oplus -1$$

$$a \cap b = b \cap a \\ a \cap (b \cap c) = (a \cap b) \cap c \\ a \cap \neg 0 = a \\ a \cap 0 = 0 \\ a \cap (b \oplus c) = (a \cap b) \oplus (a \cap c)$$

$$a \oplus b = b \oplus a \\ a \oplus (b \oplus c) = (a \oplus b) \oplus c \\ a \oplus 0 = a \\ a \oplus a = 0 \\ a \cap a = a$$

Set Lattice

$$x \subseteq y \Leftrightarrow \forall N : B_N^x \leq B_N^y \Leftrightarrow \emptyset = x \cap \neg y$$

$$x \rightarrow y \Leftrightarrow x \subset y \cap \forall z : x \subset z \Rightarrow y \subseteq z$$

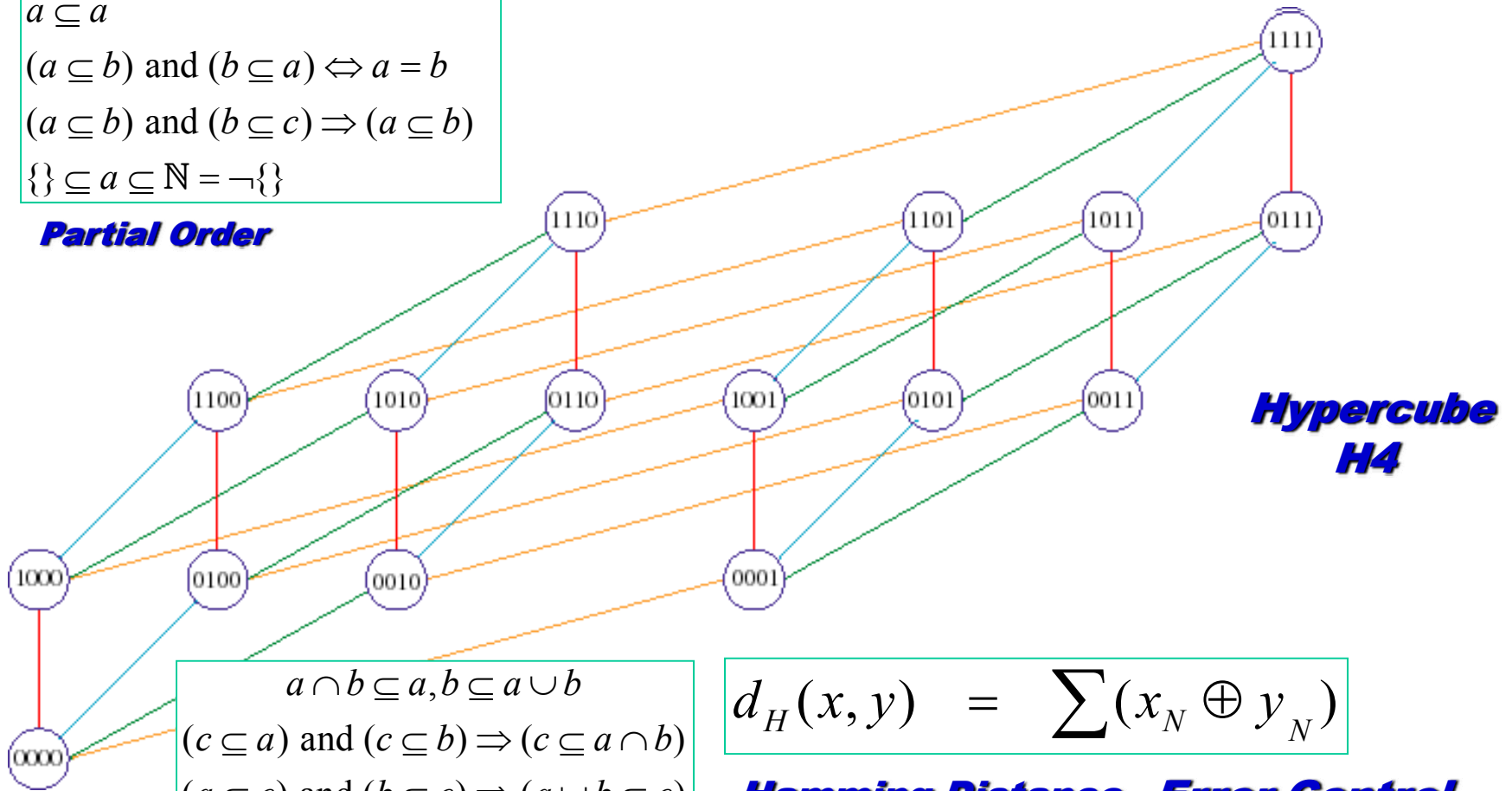
$$a \subseteq a$$

$$(a \subseteq b) \text{ and } (b \subseteq a) \Leftrightarrow a = b$$

$$(a \subseteq b) \text{ and } (b \subseteq c) \Rightarrow (a \subseteq c)$$

$$\{\} \subseteq a \subseteq \mathbb{N} = \neg \{\}$$

Partial Order



**Hypercube
H4**

$$a \cap b \subseteq a, b \subseteq a \cup b$$

$$(c \subseteq a) \text{ and } (c \subseteq b) \Rightarrow (c \subseteq a \cap b)$$

$$(a \subseteq c) \text{ and } (b \subseteq c) \Rightarrow (a \cup b \subseteq c)$$

$$d_H(x, y) = \sum (x_N \oplus y_N)$$

Hamming Distance Error Control

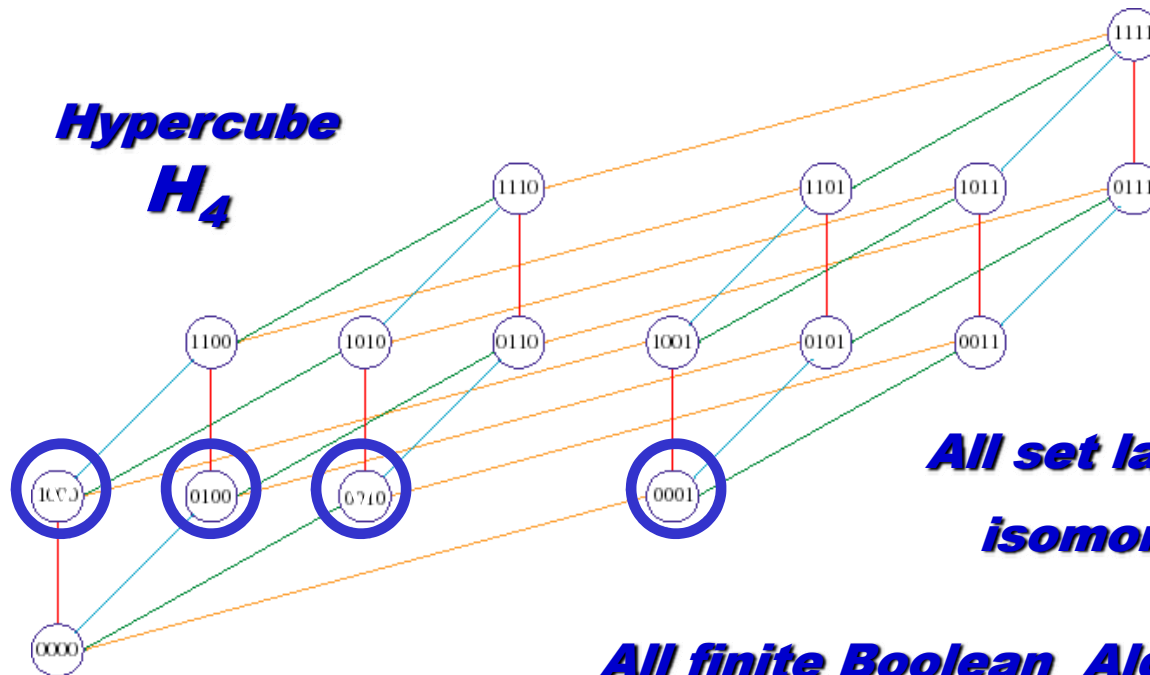
Finite Set Lattices

$$a \cap b \subseteq a, b \subseteq a \cup b$$

$$(c \subseteq a) \text{ and } (c \subseteq b) \Rightarrow (c \subseteq a \cap b)$$

$$(a \subseteq c) \text{ and } (b \subseteq c) \Rightarrow (a \cup b \subseteq c)$$

Atoms $\forall x: x \subset a \Leftrightarrow x = 0$



All set lattices with n atoms are isomorphic to Hypercube H_n

All finite Boolean Algebra have 2^n elements. Each (of these $n!$) is isomorphic to B^n .