



A Product of Shape & Sequences abstractions

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Introduction

What do we want to verify ?

When we talk about automatic static analysis of program manipulating dynamic data-structure, there are several properties we are interested in.

```

1  tree *insert(tree *t, int v) {
2      tree *m = malloc(sizeof(tree));
3      m->left = m->right = NULL;
4      m->data = v;
5      if (!t) {
6          // Empty case
7      } else {
8          tree *c = t;
9          while (v < c->data && c->left ||
10                v >= c->data && c->right)
11              if (v <= c->data) {
12                  c = c->left;
13              } else {
14                  c = c->right;
15              }
16          if (v <= c->data) {
17              c->left = m;
18          } else {
19              c->right = m;
20          }
21          return t;
22      }
23  }
```

1. No ill-pointer (null, ...) dereference "`c->`"
2. Preservation of structural invariants
"If `t` is a well-formed binary tree then so is the returned value."
3. Partial functional correctness
"If `t` is a well-formed BST, then the returned value `r` should be a well-formed BST containing the same elements as `t` plus value `v`."

Comparison of existing static analysis over dynamic data structures

Various automatic static analysis over dynamic data-structures have been proposed:

Analysis	pointer dereference	structural invariants	partial ^{fal} correctness	
			SLL	tree
Pointer analysis	✓	✗	✗	✗
Shape analysis based on...				
... 3-Value logic	✓	✓	✗	✗
e.g. [Sagiv et al. TOPLAS, 99]				
... Separation logic	✓	✓	✗	✗
e.g. [Chang et al. POPL, 08]				
... k -limited graphs	✓	✓	✓	✗
e.g. [Bouajjani et al, CAV, 10]				

None of these approaches could prove functional correctness of insertion into a binary search tree !

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How to improve the expressiveness of automatic static analysis over dynamic data-structures to prove partial functional correctness?

Separation Logic based shape analysis

[Chang et al. POPL, 2008] introduces a shape analysis based on abstract interpretation.

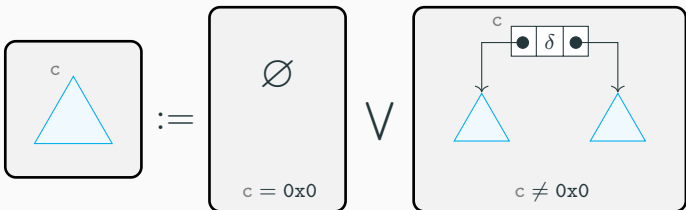
It uses a subset of **separation logic** [Reynolds, LICS 02] as an abstract representation for memory states:

- Abstract memory regions are connected with the **separating conjunction**. It expresses that these regions are disjoint

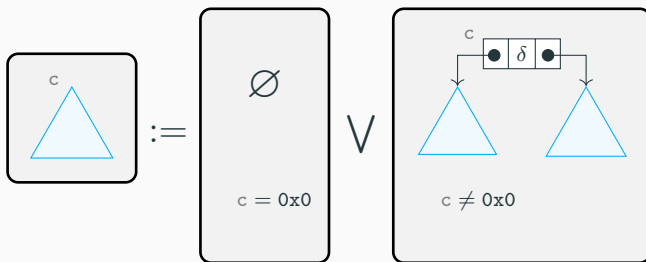
This allows to reason locally

- Inductive data-structures are synthesized by inductive predicates

Example The predicate **tree**(c), denoting a binary tree:

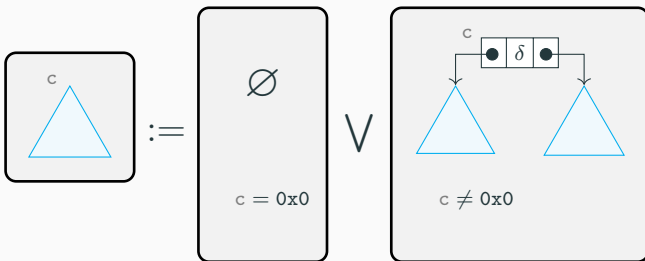


Inductive predicates are not expressive enough



\Rightarrow This predicate is expressive enough to prove memory safety & structure preservation.

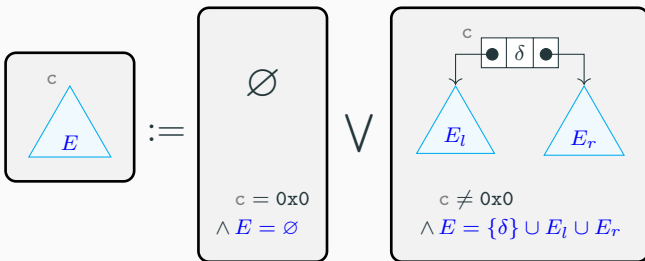
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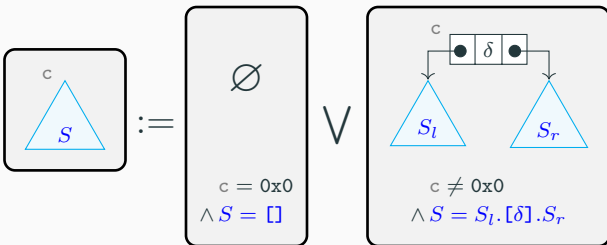
[Li et al. SAS, 2015] added **set parameters** expressing the content of data-structures.

Problem Set parameters express no constraint in respect to order of appearance !

Sequence parameters

Our solution: Express constraints on the sequence of values stored in the tree.

Add a **sequence parameter** to the inductive predicate: **tree**(c, S).



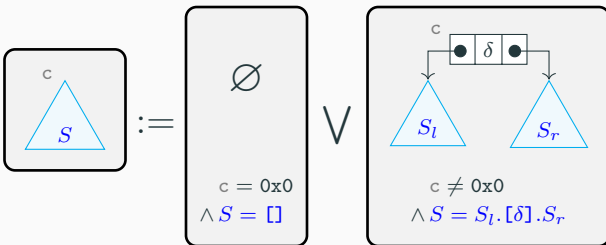
The specification of the (partial) functional correctness of `insert` can be expressed as:

$$\left\{ \begin{array}{l} \text{tree}(t, S) \\ S = \text{sort}(S) \end{array} \right\} r = \text{insert}(t, v) \left\{ \begin{array}{l} \text{tree}(r, S') \\ \text{where } S' = \text{sort}(S.[v]) \end{array} \right\}$$

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Requires to extend the shape analysis to derive precise sequence constraints.

Requires an abstract domain to reason about (possibly) sorted sequences.

Contributions

An abstract domain reasoning over sequence constraints

To reason on content with order, length constraint, extremal elements, sortedness

A Reduced product between the sequence domain and an existing shape domain

To express constraints over the content of inductive data structures

Evaluation of the analysis in the MemCAD tool

To demonstrate the gain of the expressiveness, the versatility of the approach, and discuss its cost

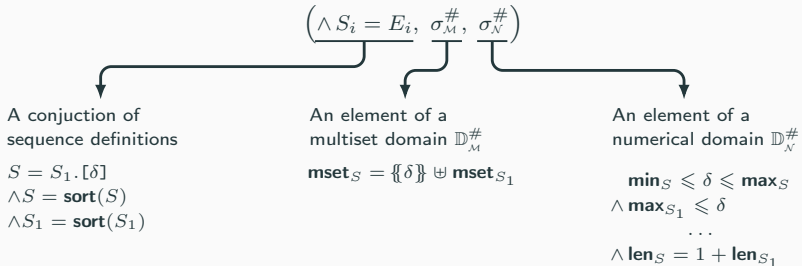
Sequence domain

Domain description

We build a domain in order to abstract sets of functions from variables to values and sequences of values:

$$\left\{ \begin{array}{l} \alpha \mapsto 2 \\ \delta \mapsto 1 \end{array} \right\} \left\{ \begin{array}{l} S \mapsto 4; 6; 1 \\ S_1 \mapsto 4; 6 \end{array} \right\}$$

An **abstract value** $\sigma_S^\#$ of the sequence abstract domain $\mathbb{D}_S^\#$ consists of:



Adding a new constraint

$$\text{guard}_s : \mathbb{D}_s^\# \rightarrow \text{seq. constraint} \rightarrow \mathbb{D}_s^\#$$

$$S = S_1.[\alpha] \wedge S = \text{sort}(S) \\ \wedge S_1 = \text{sort}(S_1)$$

To assume $S_r = [\alpha]$, guard_s follows this algorithm:

$$\wedge \text{mset}_S = \{\{\alpha\}\} \uplus \text{mset}_{S_1}$$

$$\wedge \text{len}_S = 1 + \text{len}_{S_1} + \text{len}_{S_2}$$

$$\wedge \text{min}_S \leq \alpha \leq \text{max}_S$$

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To assume $S_r = [\alpha]$, guard_s follows this algorithm:

1. add the new definition in the conjunction
2. add content/length/bounds constraints
3. fold other definitions
4. Saturate constraints

$$\frac{S = S_1 \dots S_n \quad \forall i, S_i = \text{sort}(S_i) \quad \forall i < j, \max_{S_i} \leq \min_{S_j}}{S = \text{sort}(S)}$$

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5. detect & remove cyclic constraints

Theorem: Soundness of guard_s

$\gamma_s(\text{guard}_s(\sigma_s^\#, S = E))$ contains all valuations in $\gamma_s(\sigma_s^\#)$ satisfying $S = E$.

Abstract lattice operators

- $\text{verify}_s : \mathbb{D}_s^\# \rightarrow \text{seq constraint} \rightarrow \{\mathbf{true}, \mathbf{false}\}$

$\text{verify}_s(\sigma_s^\#, S = E)$ conservatively checks if $\sigma_s^\#$ satisfies $S = E$.

- $\sqsubseteq_s : \mathbb{D}_s^\# \rightarrow \mathbb{D}_s^\# \rightarrow \{\mathbf{true}, \mathbf{false}\}$

Abstract inclusion checking, using verify_s

- $\sqcup_s : \mathbb{D}_s^\# \rightarrow \mathbb{D}_s^\# \rightarrow \mathbb{D}_s^\#$

That tries to infer common definitions from both inputs.

Example $\left(\begin{array}{l} S = S_1.S_2 \\ \wedge S_3 = [] \end{array} \right) \sqcup_s \left(\begin{array}{l} S = S_2.S_3 \\ \wedge S_1 = [] \end{array} \right) = (S = S_1.S_2.S_3)$

- $\nabla_s : \mathbb{D}_s^\# \rightarrow \mathbb{D}_s^\# \rightarrow \mathbb{D}_s^\#$

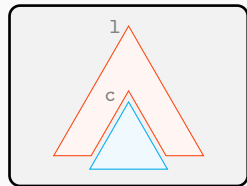
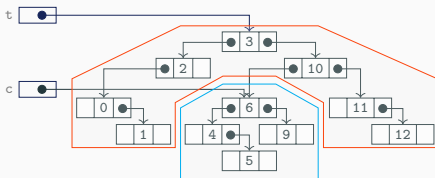
That selects the constraints in the left arguments verified in the right one.

Shape analysis with sequence predicates

Integrating sequence parameters in the shape domain

The **tree**(c) predicate only synthesizes full binary trees.

To abstract partial trees, the shape domain uses a **segment predicate** **treeseg**(l, c).



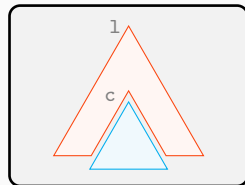
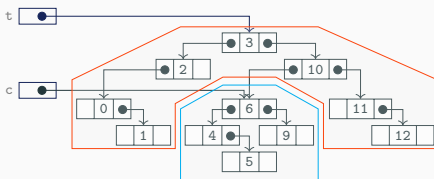
The shape domain automatically derives **treeseg** from **tree**.

The analysis must keep tracks of the content stored in the segment

Integrating sequence parameters in the shape domain

The **tree(c)** predicate only synthesizes full binary trees.

To abstract partial trees, the shape domain uses a **segment predicate** **treeseg(1, c)**.



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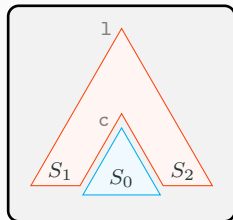
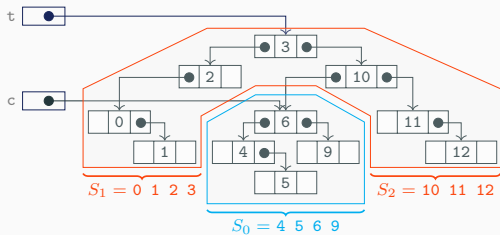
In order to reason precisely over inductive predicates, the shape analysis relies on:

- **Unfold**: refines the memory by materializing synthesized memory.
- **Fold**: extrapolates the memory state to gain generality.

Used to over-approximate two memory states

For each of these operations, the shape domain should **derive the corresponding sequence constraints to assume or verify**.

Adding sequence parameters to segment predicates



The sequence stored in the tree is: 0 1 2 3 4 5 6 9 10 11 12

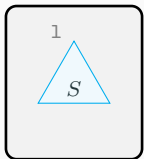
The analysis needs to recall the location of the missing sequence in **treeseg**.

⇒ the segment predicate has **two sequence parameters**: S_1, S_2

One for each side of the missing sequence

Refining abstract memory state with unfolding

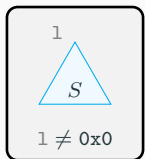
To analyze $\text{if}(1)\{v = 1 \rightarrow \text{data}\}$ with initial state $\text{tree}(1, S)$



Refining abstract memory state with unfolding

To analyze $\text{if}(1)\{v = 1 \rightarrow \text{data}\}$ with initial state $\text{tree}(1, S)$

1. The numerical constraint $1 \neq 0x0$ is guarded in the numerical part of the sequence domain.



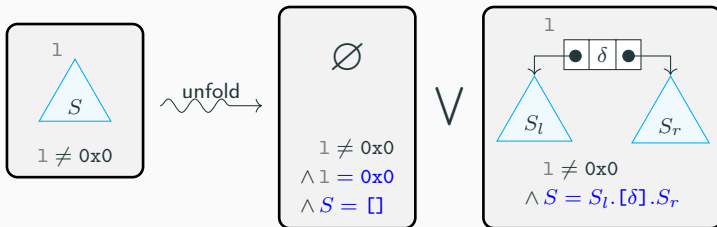
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2. To materialize $1 \rightarrow \text{data}$, the analysis **unfolds the predicate**

The abstract memory is replaced by the definition: δ, S_l, S_r are **fresh variables**

The numerical and sequences constraints are guarded in the sequence domain



Refining abstract memory state with unfolding

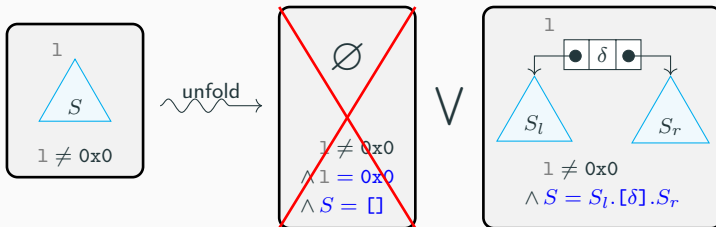
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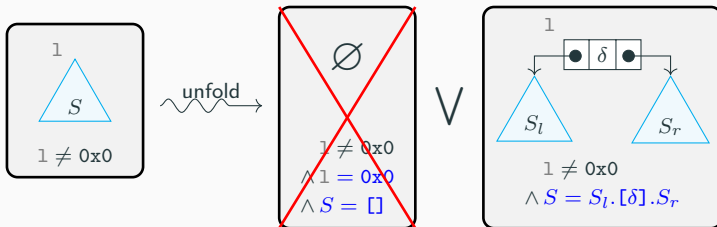
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- **The empty case:** Inconsistent with the if assumption \Rightarrow Discarded
- **The non-empty case:** $c \rightarrow \text{data}$ corresponds to δ .



Refining abstract memory state with unfolding

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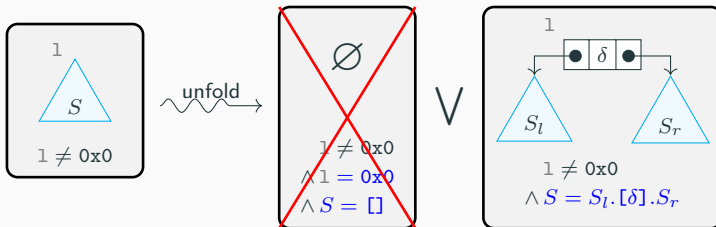
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3. The assignment $v \leftarrow \delta$ is performed.



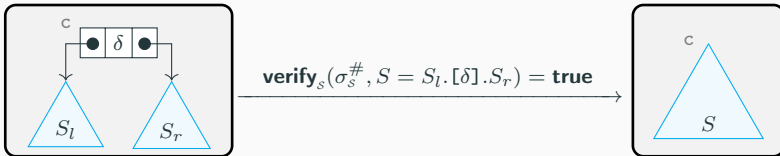
Theorem: Soundness of unfolding

The resulting disjunction of abstract states over approximates the original state.

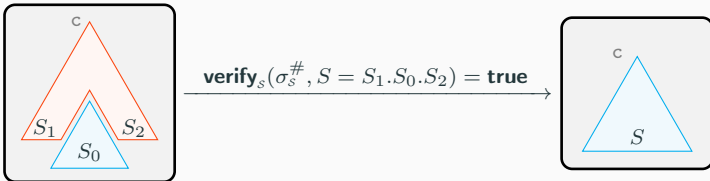
Folding the abstract state

Fold generalizes the abstract state by rewriting parts of the memory into a predicate. The analysis first checks that some constraints hold in the sequence domain.

Folding an inductive predicate



Folding segment and predicates



Theorem: Soundness of folding

The folded abstract state over-approximates the original one.

Lattice operators

Inclusion checking

Folds the left input until both are syntactically equal.

Upper bound

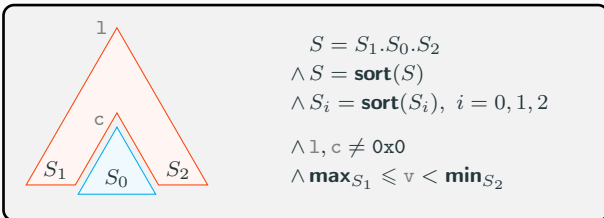
Folds both inputs until they are syntactically equal.

Widening

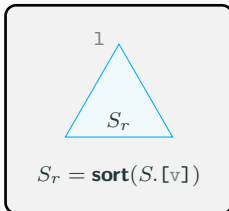
With these operators, we design a sound and automatic static analysis by forward abstract interpretation. And the analysis checks that the final state satisfies to post-condition to prove partial functional correctness.

Proving the insertion into a BST

After two iterations, the analysis inferred the following loop invariant:



Finally, the analysis was able to prove that the final state satisfies the post condition:



Experiments

Experimental Setup

The analysis described has been implemented in the MemCAD static analyzer available at gitlab.inria.fr/memcad/memcad

For each test, we specify:

- the full inductive predicates,
- the pre- and post-conditions,

Everything else (segment predicates/loop invariants) is inferred by the analysis.

(Q1) Is this analysis precise enough to prove memory safety (**Safe**) and functional properties (**Fc**) ?

(Q2) How significant is the overhead of the combined analysis compared to the baseline?

(Q3) Can this analysis successfully verify real-world C libraries?

Experiment 1: Classical list & BST programs

Example	wo/ seq	with seq parameters			
	Safe	time		Fc	
	verified	overhd.	% num	verified	
Singly linked list					
concat	Safe	2.4x	21.7%	Fc	
deep copy	Safe	1.7 x	18.1%	Fc	
length	Safe	4.7x	50.0%	Fc	
insert at position	Safe	5.4x	60.2%	Fc	
sorted insertion	Safe	6.1x	47.3%	Fc	
minimum	Safe	7.8x	45.9%	Fc	
insertion sort	Safe	29.0x	46.0%	Fc	
bubble sort	Safe	19.1x	51.5%	Fc	
merge sorted lists	Safe	9.6x	51.4%	Fc	
Binary search trees					
Insertion	Safe	6.0x	38.6%	Fc	
Delete max	Safe	6.2x	48.6%	Fc	
Search (present)	Safe	4x	45.3%	Fc	
BST to list	Safe	3.2x	38.2%	Fc	
list to BST	Safe	11.9x	46.1%	Fc	

Expressiveness

- Prove **Fc** for complex programs including 3 sorting algorithms
- Sequences improve precision for **Safe!**

Overhead

- High slowdown for complex programs
Up to 30x for insertion sort
- Most of it in the numerical domain
Quadratic cost of sortedness checking
Length constraints are expensive
- Sequence domain slows down convergence
Needs one more iteration for ∇_s to stabilize.

Experiment 2: Real-world libraries

We tested MemCAD on real-world list libraries implementing various features:

	Linux	FreeRTOS	GDSL
Circular DLL with distinguished header	Yes	Yes	Yes
Extreme sentinel nodes	No	No	Yes
Intrusive	Yes	Yes	No
Pointer to header	No	Yes	No
Length in header	No	Yes	Yes
Sorted	No	Yes	No

	Linux		FreeRTOS		GDSL	
	wo/ seq	w/ seq	wo/ seq	w/ seq	wo/ seq	w/ seq
Safe	4/4✓	4/4✓	4/4✓	4/4✓	13✓ 1X(†)	14/14✓
Fc		4/4✓		4/4✓		12✓ 2X(‡)

†: Cannot prove **Safe** for extraction at position.

‡: Cannot prove **Fc** for min/max extraction.

Conclusion

Conclusion

How to improve the expressiveness of static analysis over dynamic data-structures to prove partial functional correctness?

- Design of a novel sequence abstract domain
It leverages existing numerical/set domains to express length/bounds/content constraints.
- Integration into a separation logic based shape analysis
The reduced product derives corresponding sequence constraints for unfolding/weakening.
- Implementation in the MemCAD static analyzer
Proves partial functional correctness for complex algorithms on SLL/BST and real world libraries.

Thank you !

Could we relax the sortedness checking?

Lemma

If $S = S_1 \dots S_n$, then

$$S = \mathbf{sort}(S) \Leftrightarrow \forall i, S_i = \mathbf{sort}(S_i) \wedge \forall i < j, \mathbf{max}_{S_i} \leq \mathbf{min}_{S_j}$$

Question The number of constraints in the right-hand side is quadratic! Could we relax it for $j := i + 1$?

\Rightarrow NO ! Because of the empty sequence case !

By consistency of the concretization: $\nu_s(S) = \varepsilon \Rightarrow \begin{cases} \mathbf{max}_S = -\infty \\ \mathbf{min}_S = +\infty \end{cases}$

Consider $\nu_s = \left\{ \begin{array}{l} S \mapsto 31 \\ S_1 \mapsto 3 \\ S_2 \mapsto \varepsilon \\ S_3 \mapsto 1 \end{array} \right\}$ We have indeed:

$\nu_s \models S = S_1.S_2.S_3$	But: $\nu_s \not\models S = \mathbf{sort}(S)$
$\nu_s \models S_i = \mathbf{sort}(S_i), \forall i$	
$\nu_s \models \mathbf{max}_{S_1} \leq \mathbf{min}_{S_2}$	
$\nu_s \models \mathbf{max}_{S_2} \leq \mathbf{min}_{S_3}$	

Removing cyclic constraints

Assume the abstract state $\sigma_s^\#$ contains the following constraints:

$$\begin{aligned} S &= S_1.S'.S_2 \\ \wedge S' &= S_3.S'' \\ \wedge S'' &= S.S_4 \end{aligned}$$

If we inline definitions over S' and S'' into the definition of S we obtain:

$$S = S_1.S_3.S.S_4.S_2$$

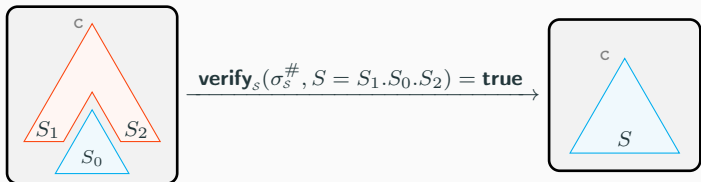
The constraints over S, S', S'' are replaced by $\left\{ \begin{array}{l} S_1 = S_2 = S_3 = S_4 = [] \\ S = S' = S'' \end{array} \right.$

If one constraint contains at least one atom $[\alpha]$, then the state is reduced to \perp_s .

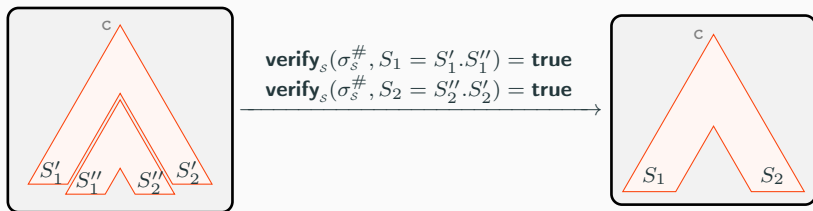
$S = \mathbf{sort}(S)$ does not count as a cyclic constraint as the implementation of the abstract domain does not represent it as such.

Concatenating inductive predicates

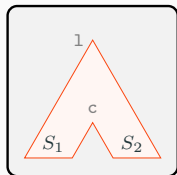
seg-full case



seg-seg case



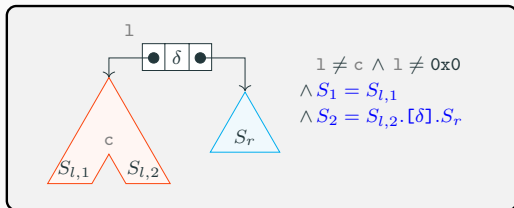
Segment tree predicate



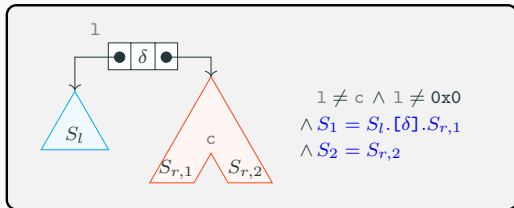
\vdash



\vee



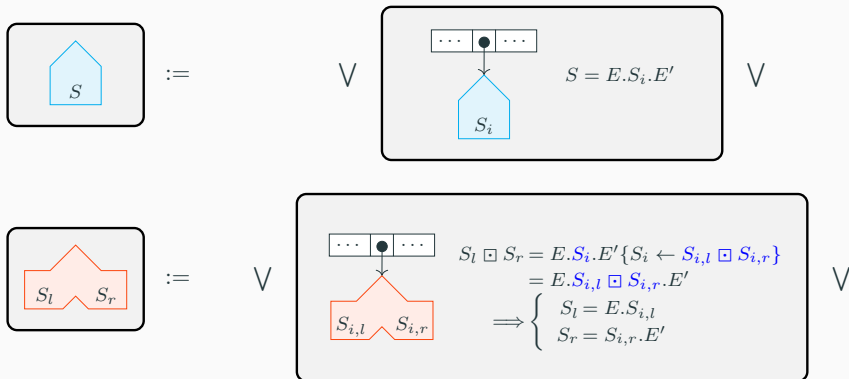
\vee



Hypothesis to derive segment from full predicate

Hypothesis

- The constraint over sequence parameter is **only concatenation based**
- The argument of each recursive call occurs **exactly once** in the constraint



Example: insertion in binary search tree

$\{\text{tree}(t, S) \wedge S = \text{sort}(S)\}$

```
if( t== NULL ){  
  // Empty case  
}else{  
  ptree c= t;  
  while( v< c->d && c->l ||  
         v>= c->d && c->r )  
    if( v < c->d ) {  
      c = c->l;  
    }else {  
      c = c->r;  
    }  
  if( v< c->d ){  
    c->l = m;  
  }else{  
    c->r = m;  
  }  
  return t;  
}
```

$\{\text{tree}(t, \text{sort}(S.[v]))\}$

$S = \text{sort}(S)$



Example: insertion in binary search tree

$\{\text{tree}(t, S) \wedge S = \text{sort}(S)\}$

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            c = c->l;  
        }else {  
            c = c->r;  
        }  
    if( v< c->d ){  
        c->l = m;  
    }else{  
        c->r = m;  
    }  
    return t;  
}
```

$\{\text{tree}(t, \text{sort}(S.[v]))\}$

$S = \text{sort}(S)$

$\wedge \alpha \neq 0x0$



Example: insertion in binary search tree

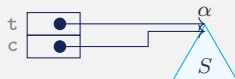
$\{\text{tree}(t, S) \wedge S = \text{sort}(S)\}$

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if( t== NULL ){  
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           v>= c->d && c->r )  
        if( v < c->d ) {  
            c = c->l  
        }else {  
            c = c->r;  
        }  
    if( v< c->d ){  
        c->l = m;  
    }else{  
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    return t;  
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$\{\text{tree}(t, \text{sort}(S.[v]))\}$

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Example: insertion in binary search tree

$$\{\text{tree}(t, S) \wedge S = \text{sort}(S)\}$$

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if( t== NULL ){
    // Empty case
}else{
    ptree c= t;
    while( (v< c->d && c->l ||
            v>= c->d && c->r )
           if( v < c->d ) {
               c = c->l;
           }else {
               c = c->r;
           }
    if( v< c->d ){
        c->l = m;
    }else{
        c->r = m;
    }
    return t;
}

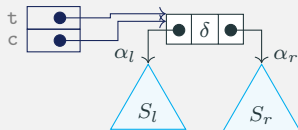
```

$$\{\text{tree}(t, \text{sort}(S.[v]))\}$$

$$S = \text{sort}(S) \wedge S = S_l.[\delta].S_r$$

$$S_i = \text{sort}(S_i) \quad i \in \{l, r\}$$

$$\wedge \alpha \neq 0x0$$

$$\max_{S_l} \leq \delta \leq \min_{S_r}$$


Example: insertion in binary search tree

$$\{\text{tree}(t, S) \wedge S = \text{sort}(S)\}$$

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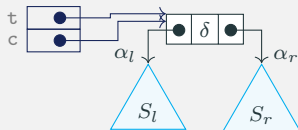
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    }
    return t;
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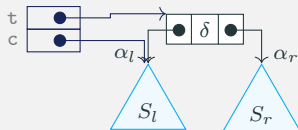
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$$S = \text{sort}(S) \wedge S = S_l.[\delta].S_r$$

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Example: insertion in binary search tree

$\{\text{tree}(t, S) \wedge S = \text{sort}(S)\}$

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    } else {
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```

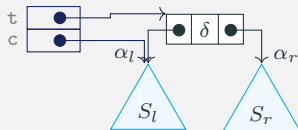
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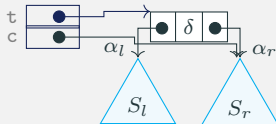


$S = \text{sort}(S) \wedge S = S_l.[\delta].S_r$

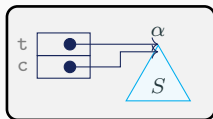
$S_i = \text{sort}(S_i) \quad i \in \{l, r\}$

$\wedge \alpha \neq 0x0 \wedge v \geq \delta \wedge \alpha_r \neq 0x0$

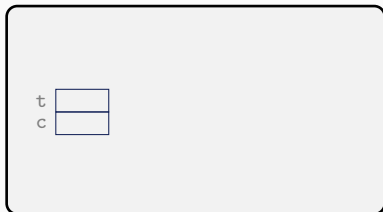
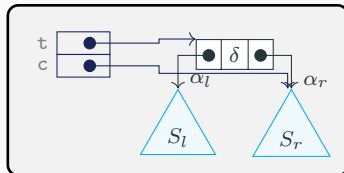
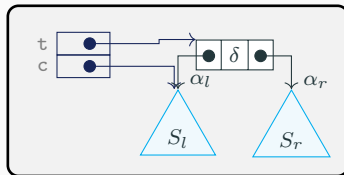
$\max_{S_l} \leq \delta \leq \min_{S_r}$



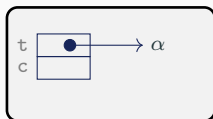
Union (Shape part)



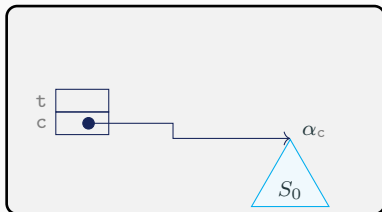
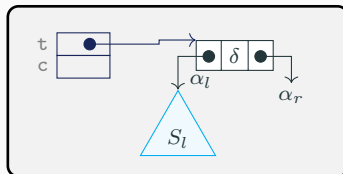
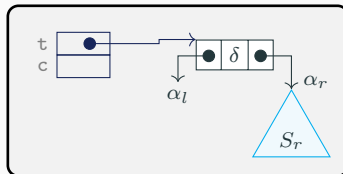
\sqcup_{Σ}



Union (Shape part)



\sqcup_{Σ}



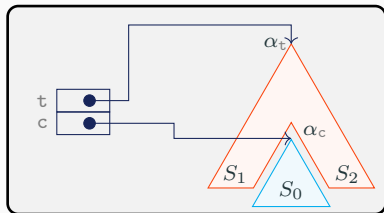
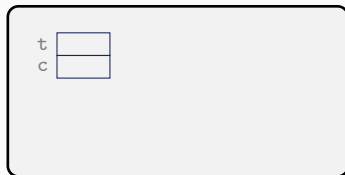
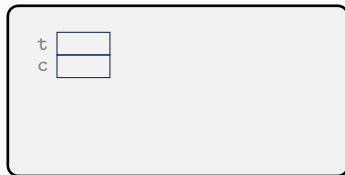
$$\alpha \leftrightarrow \alpha_c \mapsto \alpha_l, \alpha_r$$

$$S \leftrightarrow S_0 \mapsto S_l, S_r$$

Union (Shape part)



\sqcup_{Σ}



$$\alpha \leftarrow \alpha_c \mapsto \alpha_l, \alpha_r$$

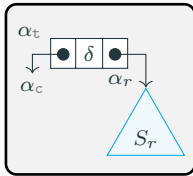
$$S \leftarrow S_0 \mapsto S_l, S_r$$

$$\alpha \leftarrow \alpha_t \mapsto \alpha, \alpha$$

$$[] \leftarrow S_1 \mapsto ??, ??$$

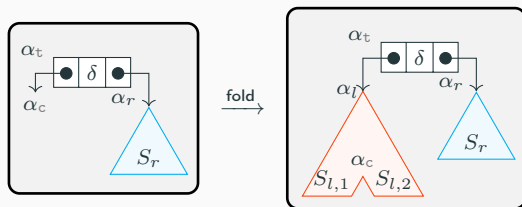
$$[] \leftarrow S_2 \mapsto ??, ??$$

Weakening: inclusion test



To verify:

Weakening: inclusion test



To verify:

$$S_{l,1} = []$$

$$S_{l,2} = []$$

$$\alpha_l = \alpha_c$$

The constraints are simplified.

$$\alpha \neq 0$$

$$\alpha_l = \alpha_l$$

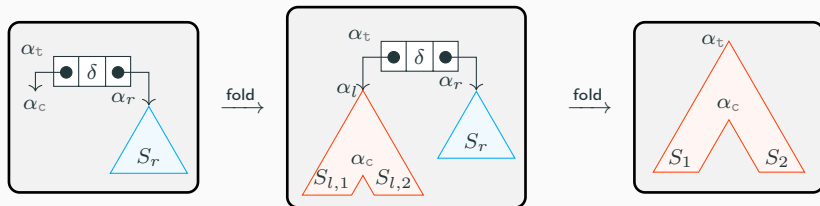
$$S_1 = []$$

$$S_2 = [\delta].S_r$$

The numerical ones are checked with **verify_s**

The sequence ones are used for definition of S_1 and S_2

Weakening: inclusion test



To verify:

$$S_{l,1} = []$$

$$S_{l,2} = []$$

$$\alpha_l = \alpha_c$$

$$S_1 = S_{l,1}$$

$$S_2 = S_{l,2} \cdot [\delta] \cdot S_r$$

$$\alpha_t \neq 0 \times 0$$

The constraints are simplified.

$$\alpha \neq 0$$

$$\alpha_l = \alpha_l$$

$$S_1 = []$$

$$S_2 = [\delta] \cdot S_r$$

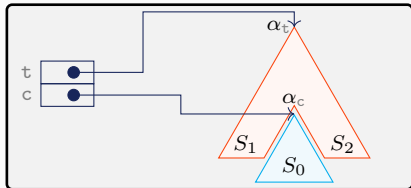
The numerical ones are checked with **verify_s**

The sequence ones are used for definition of S_1 and S_2

Union (Shape part)



\sqcup_{Σ}



$$\begin{aligned} \alpha &\leftarrow \alpha_c \mapsto \alpha_l & , \alpha_r \\ S &\leftarrow S_0 \mapsto S_l & , S_r \\ \alpha &\leftarrow \alpha_t \mapsto \alpha & , \alpha \\ \square &\leftarrow S_1 \mapsto \square & , S_l.[\delta] \\ \square &\leftarrow S_2 \mapsto [\delta].S_r, \square \end{aligned}$$

Union (Numerical part)

$$\begin{aligned}
 \alpha &\leftarrow \alpha_c \mapsto \alpha_l && , \alpha_r \\
 S &\leftarrow S_0 \mapsto S_l && , S_r \\
 \alpha &\leftarrow \alpha_t \mapsto \alpha && , \alpha \\
 [] &\leftarrow S_1 \mapsto [] && , S_l.[\delta] \\
 [] &\leftarrow S_2 \mapsto [\delta].S_l, []
 \end{aligned}$$

Result:



$$S = \mathbf{sort}(S)$$

$$\wedge \alpha \neq 0x0$$

\sqcup_{Σ}

$$S = \mathbf{sort}(S) \wedge S = S_l.[\delta].S_r$$

$$S_i = \mathbf{sort}(S_i) \quad i \in \{l, r\}$$

$$\wedge \alpha \neq 0x0$$

$$\wedge \alpha_l \neq 0x0$$

$$\wedge v < \delta$$

$$\mathbf{max}_{S_l} \leq \delta \leq \mathbf{min}_{S_r}$$

$$S = \mathbf{sort}(S) \wedge S = S_l.[\delta].S_r$$

$$S_i = \mathbf{sort}(S_i) \quad i \in \{l, r\}$$

$$\wedge \alpha \neq 0x0$$

$$\wedge \alpha_r \neq 0x0$$

$$\wedge v \geq \delta$$

$$\mathbf{max}_{S_l} \leq \delta \leq \mathbf{min}_{S_r}$$

Union (Numerical part)

$$\begin{aligned}
 \alpha &\leftarrow \alpha_c \mapsto \alpha_l && , \alpha_r \\
 S &\leftarrow S_0 \mapsto S_l && , S_r \\
 \alpha &\leftarrow \alpha_t \mapsto \alpha && , \alpha \\
 [] &\leftarrow S_1 \mapsto [] && , S_l.[\delta] \\
 [] &\leftarrow S_2 \mapsto [\delta].S_l, [] &&
 \end{aligned}$$

Result:



$$S = \mathbf{sort}(S)$$

$$S_1 = S_2 = []$$

$$\wedge \alpha = \alpha_c = \alpha_t \neq 0x0$$

\sqcup_{Σ}

$$S = \mathbf{sort}(S) \wedge S = S_0.S_2$$

$$S_i = \mathbf{sort}(S_i) \quad i \in \{l, r, 1, 2\}$$

$$S_0 = S_l \wedge S_1 = [] \wedge S_2 = [\delta].S_r$$

$$\wedge \alpha = \alpha_t \neq 0x0$$

$$\wedge \alpha_l = \alpha_c \neq 0x0$$

$$\wedge \forall < \delta = \mathbf{min}_{S_2} \wedge \mathbf{max}_{S_1} = -\infty$$

$$\mathbf{max}_{S_l} \leq \delta \leq \mathbf{min}_{S_r}$$

$$S = \mathbf{sort}(S) \wedge S = S_1.S_0$$

$$S_i = \mathbf{sort}(S_i) \quad i \in \{l, r, 1, 2\}$$

$$S_0 = S_r \wedge S_1 = S_l.[\delta] \wedge S_2 = []$$

$$\wedge \alpha = \alpha_t \neq 0x0$$

$$\wedge \alpha_r = \alpha_c \neq 0x0$$

$$\wedge \forall \geq \delta = \mathbf{max}_{S_1} \wedge \mathbf{min}_{S_2} = +\infty$$

$$\mathbf{max}_{S_l} \leq \delta \leq \mathbf{min}_{S_r}$$

Union (Numerical part)

$$\begin{aligned}
 \alpha &\leftarrow \alpha_c \mapsto \alpha_l & , \alpha_r \\
 S &\leftarrow S_0 \mapsto S_l & , S_r \\
 \alpha &\leftarrow \alpha_t \mapsto \alpha & , \alpha \\
 [] &\leftarrow S_1 \mapsto [] & , S_l.[\delta] \\
 [] &\leftarrow S_2 \mapsto [\delta].S_l, [] &
 \end{aligned}$$

Result:

$$S_i = \mathbf{sort}(S_i) \quad i \in \{_, 0, 1, 2\}$$

$$\begin{aligned}
 S &= \mathbf{sort}(S) \\
 S_1 &= S_2 = [] \\
 \wedge \alpha &= \alpha_c = \alpha_t \neq 0x0
 \end{aligned}$$

\sqcup_{Σ}

$$\begin{aligned}
 S &= \mathbf{sort}(S) \wedge S = S_0.S_2 \\
 S_i &= \mathbf{sort}(S_i) \quad i \in \{l, r, 1, 2\} \\
 S_0 &= S_l \wedge S_1 = [] \wedge S_2 = [\delta].S_r \\
 \wedge \alpha &= \alpha_t \neq 0x0 \\
 \wedge \alpha_l &= \alpha_c \neq 0x0 \\
 \wedge \forall &< \delta = \mathbf{min}_{S_2} \wedge \mathbf{max}_{S_1} = -\infty \\
 \mathbf{max}_{S_l} &\leq \delta \leq \mathbf{min}_{S_r}
 \end{aligned}$$

$$\begin{aligned}
 S &= \mathbf{sort}(S) \wedge S = S_1.S_0 \\
 S_i &= \mathbf{sort}(S_i) \quad i \in \{l, r, 1, 2\} \\
 S_0 &= S_r \wedge S_1 = S_l.[\delta] \wedge S_2 = [] \\
 \wedge \alpha &= \alpha_t \neq 0x0 \\
 \wedge \alpha_r &= \alpha_c \neq 0x0 \\
 \wedge \forall &\geq \delta = \mathbf{max}_{S_1} \wedge \mathbf{min}_{S_2} = +\infty \\
 \mathbf{max}_{S_l} &\leq \delta \leq \mathbf{min}_{S_r}
 \end{aligned}$$

Union (Numerical part)

$$\begin{aligned}
 \alpha &\leftarrow \alpha_c \mapsto \alpha_l & , \alpha_r \\
 S &\leftarrow S_0 \mapsto S_l & , S_r \\
 \alpha &\leftarrow \alpha_t \mapsto \alpha & , \alpha \\
 [] &\leftarrow S_1 \mapsto [] & , S_l.[\delta] \\
 [] &\leftarrow S_2 \mapsto [\delta].S_l, [] &
 \end{aligned}$$

Result:

$$\begin{aligned}
 S_i &= \mathbf{sort}(S_i) \quad i \in \{_, 0, 1, 2\} \\
 S &= S_1.S_0.S_2
 \end{aligned}$$

$$\begin{aligned}
 S &= \mathbf{sort}(S) \\
 S_1 &= S_2 = [] \\
 \wedge \alpha &= \alpha_c = \alpha_t \neq 0x0
 \end{aligned}$$

\sqcup_Σ

$$\begin{aligned}
 S &= \mathbf{sort}(S) \wedge S = S_0.S_2 \\
 S_i &= \mathbf{sort}(S_i) \quad i \in \{l, r, 1, 2\} \\
 S_0 &= S_l \wedge S_1 = [] \wedge S_2 = [\delta].S_r \\
 \wedge \alpha &= \alpha_t \neq 0x0 \\
 \wedge \alpha_l &= \alpha_c \neq 0x0 \\
 \wedge \forall &< \delta = \mathbf{min}_{S_2} \wedge \mathbf{max}_{S_1} = -\infty \\
 \mathbf{max}_{S_l} &\leq \delta \leq \mathbf{min}_{S_r}
 \end{aligned}$$

$$\begin{aligned}
 S &= \mathbf{sort}(S) \wedge S = S_1.S_0 \\
 S_i &= \mathbf{sort}(S_i) \quad i \in \{l, r, 1, 2\} \\
 S_0 &= S_r \wedge S_1 = S_l.[\delta] \wedge S_2 = [] \\
 \wedge \alpha &= \alpha_t \neq 0x0 \\
 \wedge \alpha_r &= \alpha_c \neq 0x0 \\
 \wedge \forall &\geq \delta = \mathbf{max}_{S_1} \wedge \mathbf{min}_{S_2} = +\infty \\
 \mathbf{max}_{S_l} &\leq \delta \leq \mathbf{min}_{S_r}
 \end{aligned}$$

Union (Numerical part)

$$\begin{aligned}
 \alpha &\leftarrow \alpha_c \mapsto \alpha_l & , \alpha_r \\
 S &\leftarrow S_0 \mapsto S_l & , S_r \\
 \alpha &\leftarrow \alpha_t \mapsto \alpha & , \alpha \\
 [] &\leftarrow S_1 \mapsto [] & , S_l.[\delta] \\
 [] &\leftarrow S_2 \mapsto [\delta].S_l, [] &
 \end{aligned}$$

Result:

$$\begin{aligned}
 S_i &= \mathbf{sort}(S_i) \quad i \in \{_, 0, 1, 2\} \\
 S &= S_1.S_0.S_2 \\
 \alpha_c, \alpha_t &\neq 0x0
 \end{aligned}$$

$$\begin{aligned}
 S &= \mathbf{sort}(S) \\
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 \end{aligned}$$

\sqcup_Σ

$$\begin{aligned}
 S &= \mathbf{sort}(S) \wedge S = S_0.S_2 \\
 S_i &= \mathbf{sort}(S_i) \quad i \in \{l, r, 1, 2\} \\
 S_0 &= S_l \wedge S_1 = [] \wedge S_2 = [\delta].S_r \\
 \wedge \alpha &= \alpha_t \neq 0x0 \\
 \wedge \alpha_l &= \alpha_c \neq 0x0 \\
 \wedge \forall &< \delta = \mathbf{min}_{S_2} \wedge \mathbf{max}_{S_1} = -\infty \\
 \mathbf{max}_{S_l} &\leq \delta \leq \mathbf{min}_{S_r}
 \end{aligned}$$

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 S &= \mathbf{sort}(S) \wedge S = S_1.S_0 \\
 S_i &= \mathbf{sort}(S_i) \quad i \in \{l, r, 1, 2\} \\
 S_0 &= S_r \wedge S_1 = S_l.[\delta] \wedge S_2 = [] \\
 \wedge \alpha &= \alpha_t \neq 0x0 \\
 \wedge \alpha_r &= \alpha_c \neq 0x0 \\
 \wedge \forall &\geq \delta = \mathbf{max}_{S_1} \wedge \mathbf{min}_{S_2} = +\infty \\
 \mathbf{max}_{S_l} &\leq \delta \leq \mathbf{min}_{S_r}
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 S &= S_1.S_0.S_2 \\
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 \mathbf{max}_{S_1} &\leq_v \leq \mathbf{min}_{S_2}
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\sqcup_Σ

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 \wedge v &< \delta = \mathbf{min}_{S_2} \wedge \mathbf{max}_{S_1} = -\infty \\
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 \wedge v &\geq \delta = \mathbf{max}_{S_1} \wedge \mathbf{min}_{S_2} = +\infty \\
 \mathbf{max}_{S_l} &\leq \delta \leq \mathbf{min}_{S_r}
 \end{aligned}$$

Example: insertion in binary search tree

$$\left\{ \begin{array}{l} \text{tree}(t, S) \\ \wedge S = \text{sort}(S) \end{array} \right\}$$

```

if( t== NULL ){
    // Empty case
}else{
    ptree c= t;
    while( (v< c->d && c->l ||
            v>= c->d && c->r )
        if( v < c->d ) {
            c = c->l
        }else {
            c = c->r;
        }
    if( v< c->d ){
        c->l = m;
    }else{
        c->r = m;
    }
    return t;
}

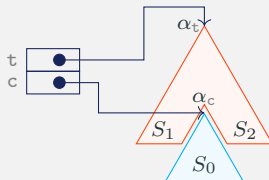
```

$$\{ \text{tree}(t, \text{sort}(S.[v])) \}$$

$$S_i = \text{sort}(S_i) \quad i \in \{_, 1, 2\}$$

$$S = S_1.S_0.S_2$$

$$\alpha_c, \alpha_t \neq 0x0$$

$$\max_{S_1} \leq v \leq \min_{S_2}$$


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        }
    if( v< c->d ){
        c->l = m;
    }else{
        c->r = m;
    }
    return t;
}

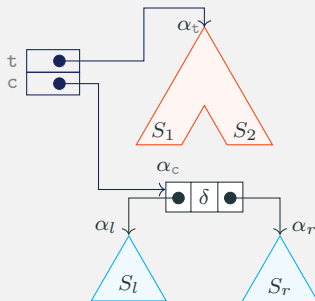
```

$$\{\text{tree}(t, \text{sort}(S.[v]))\}$$

$$S_i = \text{sort}(S_i) \quad i \in \{_, 1, 2\}$$

$$S = S_1.S_0.S_2$$

$$\alpha_c, \alpha_t \neq 0x0$$

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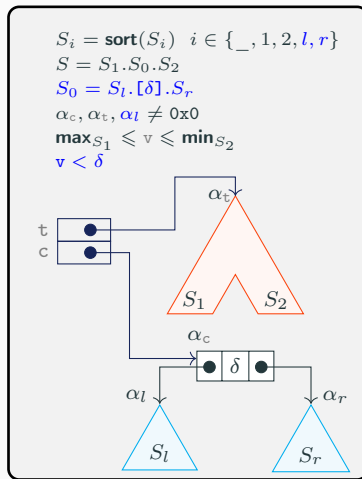
Example: insertion in binary search tree

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  return t;
}

```

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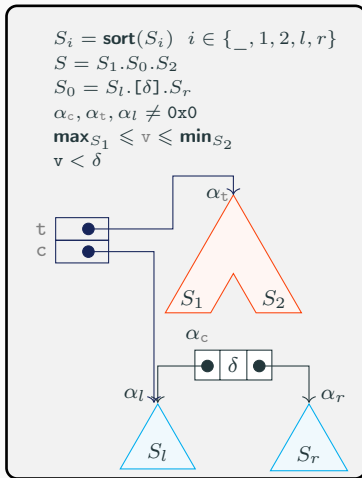
Example: insertion in binary search tree

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        c->l = m;
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        c->r = m;
    }
    return t;
}

```

$$\{\text{tree}(t, \text{sort}(S.[v]))\}$$


Example: insertion in binary search tree

$$S_i = \text{sort}(S_i) \quad i \in \{_, 1, 2\}$$

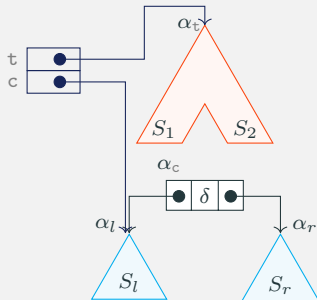
$$S = S_1.S_0.S_2$$

$$S_0 = S_l.[\delta].S_r$$

$$\alpha_c, \alpha_t \neq 0x0$$

$$\max_{S_1} \leq v \leq \min_{S_2}$$

$$v < \delta$$



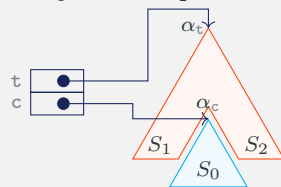
$\subseteq \Sigma$

$$S_i = \text{sort}(S_i) \quad i \in \{_, 1, 2\}$$

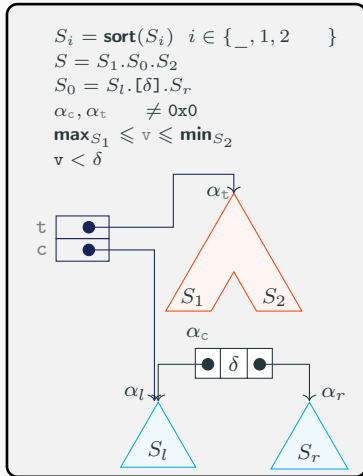
$$S = S_1.S_0.S_2$$

$$\alpha_c, \alpha_t \neq 0x0$$

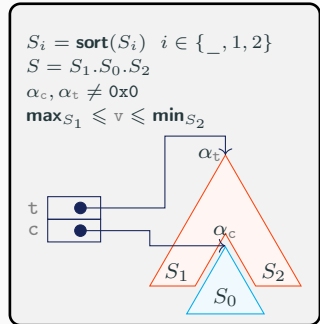
$$\max_{S_1} \leq v \leq \min_{S_2}$$



Example: insertion in binary search tree



\sqsubseteq_Σ



⇒ Invariant found after two iterations !

Example: insertion in binary search tree

$$S_i = \text{sort}(S_i) \quad i \in \{_, 1, 2, l, r\}$$

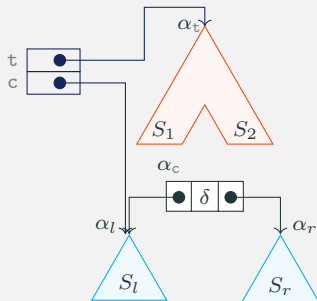
$$S = S_1.S_0.S_2$$

$$S_0 = S_l.[\delta].S_r$$

$$\alpha_c, \alpha_t, \alpha_l \neq 0x0$$

$$\max_{S_1} \leq v \leq \min_{S_2}$$

$$v < \delta$$



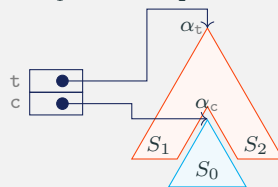
\sqsubseteq_Σ

$$S_i = \text{sort}(S_i) \quad i \in \{_, 1, 2\}$$

$$S = S_1.S_0.S_2$$

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$$\max_{S_1} \leq v \leq \min_{S_2}$$



⇒ Invariant found after two iterations !