Programming hybrid systems with synchronous languages

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 - 3 INRIA







CSDM 2011, December 7–9, Paris

Reactive systems

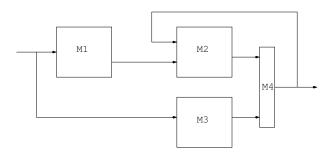
- ▶ They react continuously to the external environment.
- At the speed imposed by this environment.
- Statically bounded memory and response time.

Conciliate three notions in the programming model:

- Parallelism, concurrency while preserving determinism.
 e.g, control at the same time rolling and pitching
 → parallel description of the system
- Strong temporal constraints.
 e.g, the physics does not wait!

 → temporal constraints should be expressed in the system

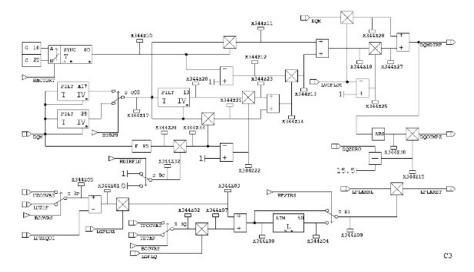
Synchronous Kahn Networks



- parallel processes communicating through data-flows
- communication in zero time: data is available as soon as it is produced.
- a global logical time scale even though individual rhythms may differ
- ▶ these drawings are not so different from actual computer programs

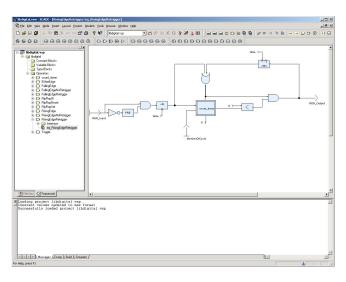
SAO (Spécification Assistée par Ordinateur)—Airbus 80's

Describe the system as block diagrams (synchronous communicating machines)



SCADE 4 (Safety Critical Application Development Env. – Esterel-Tech.)

From computer assisted drawings to executable (sequential/parallel) code!



Caspi, Pilaud, Halbwachs, and Plaice. Lustre: A Declarative Language for Programming Synchronous Systems. 1987.

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Programming with streams

Caspi, Pilaud, Halbwachs, and Plaice. Lustre: A Declarative Language for Programming Synchronous Systems. 1987.

constants 1 = 1 1 1 1 ... operators
$$x + y = x_0 + y_0$$
 $x_1 + y_1$ $x_2 + y_2$ $x_3 + y_3$... $(z = x + y \text{ means that at every instant } i : z_i = x_i + y_i)$

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constants
$$1=1$$
 1 1 1 \cdots operators $x+y=x_0+y_0$ x_1+y_1 x_2+y_2 x_3+y_3 \cdots $(z=x+y)$ means that at every instant $i:z_i=x_i+y_i)$ unit delay 0 fby $(x+y)=0$ x_0+y_0 x_1+x_1 x_2+x_2 \cdots

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unit delay 0 fby $(x + y) = 0 \quad x_0 + y_0 \quad x_1 + x_1 \quad x_2 + x_2 \quad \cdots$

pre $(x + y) = nil \quad x_0 + y_0 \quad x_1 + x_1 \quad x_2 + x_2 \quad \cdots$

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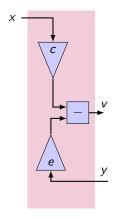
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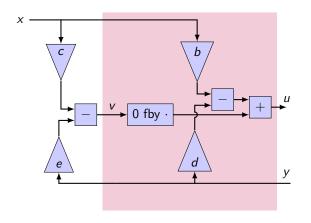
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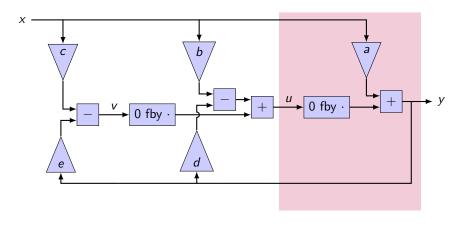
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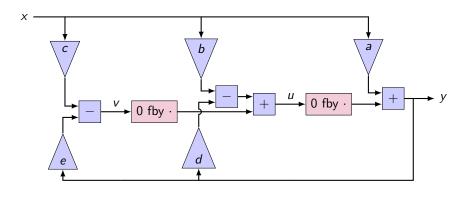
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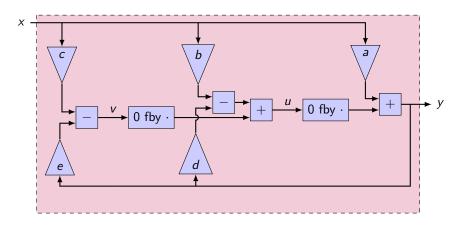




$$u = b * x - d * y + (0.0 \text{ fby } v)$$
 and $v = c * x - e * y$







let node iir_filter_2
$$x = y$$
 where
$$rec \ y = a * x + (0.0 \ fby \ u)$$
 and $u = b * x - d * y + (0.0 \ fby \ v)$ and $v = c * x - e * y$

- ► A simple and pure notion of execution in discrete time
- Parallel composition is
 - well-defined
 - deterministic: very important in practice for reproducibility
- Parallelism is compiled: programs can be translated into efficient sequential
- ▶ The code executes in bounded memory and bounded time
- ▶ Programs are finite-state and can be verified by model-checking

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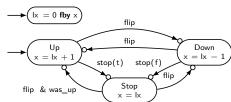
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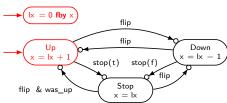
Lustre can be extended in several ways...

```
let node counter (flip, stop) = x
where
 rec lx = 0 fby x
 and automaton
       Up \rightarrow
        do
          x = Ix + 1
         until flip then Down
               stop then Stop(true)
        done
       Down \rightarrow
        do
          x = |x - 1|
         until flip then Up
               stop then Stop(false)
        done
       Stop(was_up) →
        dο
          x = Ix
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- Parallel composition of dataflow equations and automata
- x has a different definition in each mode
- ▶ But only a single definition in a reaction

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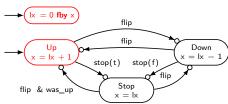


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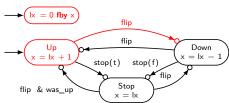


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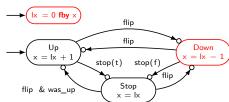
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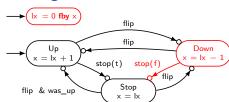
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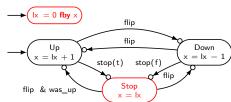
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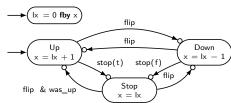
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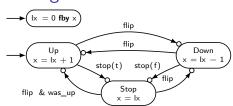
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- Automata are just a convenient syntax
- ► They can be reduced to discrete dataflow equations by a source-to-source transformation

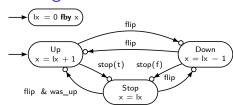
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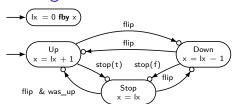
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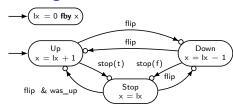
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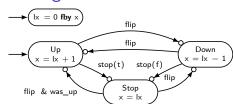


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lexing/ parsing
typing/ caus./init.

automata

→

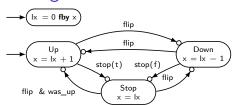
scheduling

code gen.

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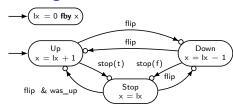


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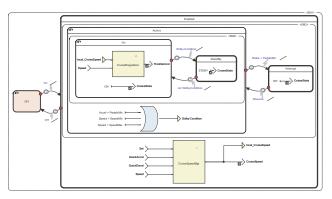
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SCADE 6





- Industrial version of Lustre/Lucid Synchrone with automata
- ▶ Used in critical systems (DO-178B certified)
- ► Airbus flight control; Train (interlocking, on-board); Nuclear safety

So, what's left to do?

- We want a language for programming complex discrete systems and modelling their physical environments
- ► (Also: embedded software that includes physical models)

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So, what's left to do?

- We want a language for programming complex discrete systems and modelling their physical environments
- ► (Also: embedded software that includes physical models)

- Something like Simulink/Stateflow, but
 - Simpler and more consistent semantics and compilation
 - ▶ Better understand interactions between discrete and continuous
 - Simpler treatment of automata
 - Certifiability for the discrete parts

Understand and improve the design of such modelling tools





Lee and Zheng. Leveraging synchronous language principles for heterogeneous modeling and design of embedded systems. EMSOFT'07.





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Ptolemy and HyVisual

- ▶ Programming languages perspective
- Numerical solvers as directors
- Working tool and examples





Lee and Zheng. Leveraging synchronous language principles for heterogeneous modeling and design of embedded systems. EMSOFT'07.

Carloni et al. Languages and tools for hybrid systems design. 2006.

Simulink/Stateflow

- ► Simulation *→* development
- two distinct simulation engines
- semantics & consistency: non-obvious





Lee and Zheng. Leveraging synchronous language principles for heterogeneous modeling and design of embedded systems. EMSOFT'07.

Our approach

- ► Source-to-source compilation
- ► Automata <>> data-flow
- ► Extend other languages (SCADE 6)

Approach

- Add Ordinary Differential Equations to an existing synchronous language
- ▶ Two concrete reasons:
 - Increase modelling power (hybrid programming)
 - ► Exploit existing compiler (target for code generation)
- ► Simulate with an external off-the-shelf numerical solver (Sundials CVODE, Hindmarsh et al. SUNDIALS: Suite of nonlinear and) differential/algebraic equation solvers. 2005.
- Conservative extension: synchronous functions are compiled, optimized, and executed as per usual.

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discrete synchronous language: assume infinitely fast execution

 $\rightarrow \mathbb{N}$

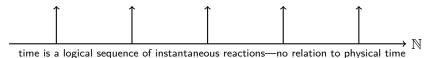




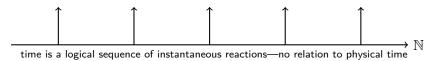




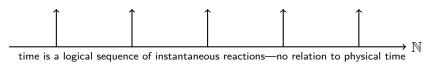




discrete synchronous language: assume infinitely fast execution



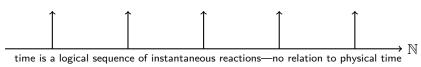
discrete synchronous language: assume infinitely fast execution



hybrid synchronous language: assume infinitely precise base clock

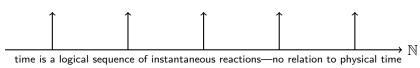
 $^*\mathbb{R}$

discrete synchronous language: assume infinitely fast execution



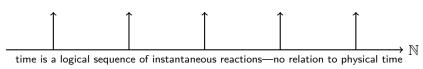


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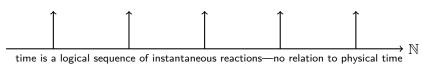


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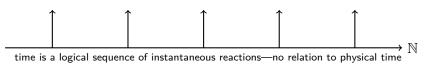


discrete synchronous language: assume infinitely fast execution

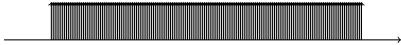




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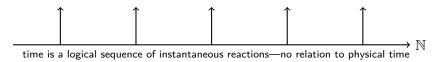


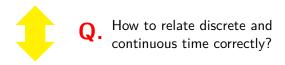
hybrid synchronous language: assume infinitely precise base clock



assume an *infinitesimal* increment of the base clock—a **non-standard** semantics

discrete synchronous language: assume infinitely fast execution





hybrid synchronous language: assume infinitely precise base clock



assume an $\it infinite simal$ increment of the base clock—a $\it non-standard$ semantics

Q. How to simulate effectively?

Given:

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let node sum(x) = cpt where
rec cpt = (0.0 \text{ fby } cpt) +. x
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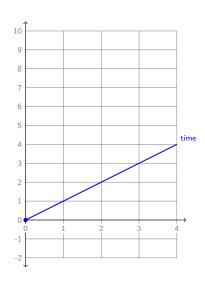
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- ▶ Option 1: $\mathbb{N} \subseteq \mathbb{R}$
- ▶ Option 2: depends on solver
- ▶ Option 3: infinitesimal steps
- ▶ Option 4: type and reject



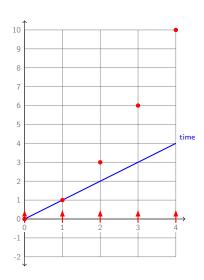
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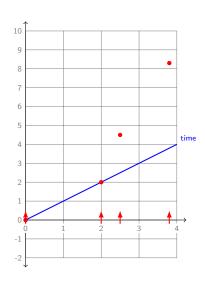
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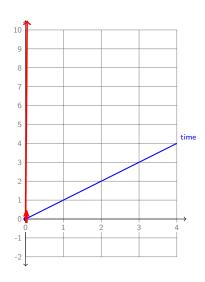
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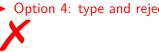
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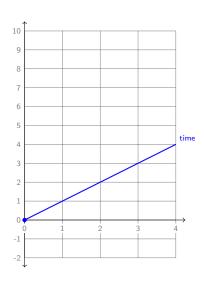
```
let node sum(x) = cpt where
  rec cpt = (0.0 \text{ fby cpt}) + ... \times
```

Evaluate:

```
der time = 1.0 init 0.0
and
y = sum(time)
```

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Which programs make sense?

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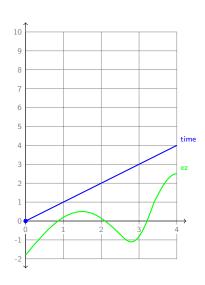
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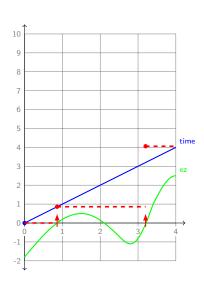
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Interpretation:

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Explicitly relate simulation and logical time (using zero-crossings)

Try to minimize the effects of solver parameters and choices

Basic typing

Milner-like type system

The type language

$$\begin{array}{lll} bt & ::= & \texttt{float} \mid \texttt{int} \mid \texttt{bool} \mid \texttt{zero} \\ t & ::= & bt \mid t \times t \mid \beta \\ \sigma & ::= & \forall \beta_1, ..., \beta_n.t \xrightarrow{k} t \\ k & ::= & \texttt{D} \mid \textbf{C} \mid \textbf{A} \end{array}$$

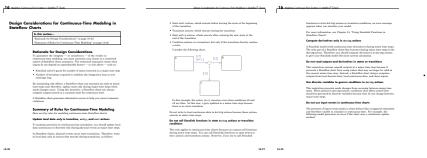


Initial conditions

$$\begin{array}{lll} (+) & : & \operatorname{int} \times \operatorname{int} \xrightarrow{\mathbb{A}} \operatorname{int} \\ (=) & : & \forall \beta.\beta \times \beta \xrightarrow{\mathbb{A}} \operatorname{bool} \\ \operatorname{if} & : & \forall \beta.\operatorname{bool} \times \beta \times \beta \xrightarrow{\mathbb{A}} \beta \\ \cdot \operatorname{fby} \cdot & : & \forall \beta.\beta \times \beta \xrightarrow{\mathbb{D}} \beta \\ \operatorname{up}(\cdot) & : & \operatorname{float} \xrightarrow{\mathbb{C}} \operatorname{zero} \\ \end{array}$$

What about continuous automata?

Stateflow User's Guide The Mathworks, pages 16-26 to 16-29, 2011.



- 'Restricted subset of Stateflow chart semantics'
 - restricts side-effects to major time steps
 - supported by warnings and errors in tool (mostly)
- ► Our D/C/A/zero system extends naturally for the same effect
- ► For both discrete (synchronous) and continuous (hybrid) contexts

Compilation: source-to-source transformation

```
let hybrid ball () =
           let
           rec der v = (-. g / m) init v0
                        reset (-. 0.8 *. last v) every up(-. h)
h
           and der h = v init h0
           in (v, h)
```

```
Compilation: source-to-source transformation
             let hybrid ball () =
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                            reset (-...0.8 *. last v) every up(-...h)
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               and der h = v init h0
               in (v, h)
             let node ball (z1, (lh, lv), ()) =
               let rec i = true fbv false
               and dv = (-, g / m)
               and v = if i then v0
                        else if z1 then -. 0.8 *. lv
                        else Iv
               and dh = v
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and v = if i then v0
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transform into discrete subset , upz1, (h, v), (dh, dv))

and dh = v

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Demonstrations

- ► Bouncing ball (standard)
- ► Bang-bang temperature controller (Simulink/Stateflow)
- Sticky Masses (Ptolemy)

- Synchronous languages should and can properly treat hybrid systems
- ▶ There are three good reasons for doing so
 - To exploit existing compilers and techniques
 - 2. For programming the discrete subcomponents
 - 3. To clarify underlying principles and guide language design/semantics
- ► Our approach
 - 1. Synchronous data-flow language with automata and ODEs
 - 2. Static type system to separate discrete from continuous behaviors
 - 3. Relate discrete to continuous via zero-crossings
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- Language
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Longer term

- Clock calculus: an finer analysis of piece-wise continuous/continuous/discrete
- ► Causality analysis: (partial) detection of discrete Zeno-behavior
- Semantics: how abstract is the solver?
- Real-time simulation (trade-off accuracy and execution time)

Bibliography at: www.di.ens.fr/ pouzet